

ASJ

# DATA SHEET

## High-Power Thick Film Chip Resistor

### CPW Series

0.1% TO 5%, TCR -200 TO +400

SIZE: 0201/0402/0603/0805/1206/1210/2010/2512

RoHS-Compliant



# HIGH POWER RATING CHIP RESISTOR

CPW Series

DS-ENG-010

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## 1. SCOPE

- 1.1 This specification is applicable to Lead-free and Halogen-free of RoHS Directive for CPW series high power thick film chip resistors.
- 1.2 The product is for general electronic purpose.
- 1.3 The products are tested and passed based on the test conditions and methods defined in AEC-Q200.

## 2. PART NUMBERING SYSTEM

Part Numbering is made in accordance with the following system:

Ordering Code / Information

CPW	10	-	XXXX	-	F	K
Type	Size (Inch / mm)	Nominal Resistance			Resistance Tolerance	Packaging
High Power Thick Film Chip Resistors	05(0201/0603) 10(0402/1005) 16(0603/1608) 21(0805/2012) 32(1206/3216) 40(1210/3225) 50(2010/5025) 63(2512/6432)	Resistors	5% (3-Digit)	EX. 10Ω=100 4.7Ω=4R7 JUMPER=000	B=±0.1% D=0.5% F=±1% J=±5% Z=Zero Ohm	E = 4,000 pcs Lead Free L = 5,000 pcs Lead Free K = 10,000 pcs Lead Free Y = 20,000 pcs Lead Free N = 50,000 pcs Lead Free
	0.1% 0.5% 1% (4-Digit)		EX. 10.2Ω=10R2 10KΩ=1002 JUMPER=0000			

## 3. RATING

### 3.1 Rated Power

#### 3.1.1 Resistor Rated Power

Type	Rated Power at 70°C	Max. Working Voltage	Max. Overload Voltage	JUMPER Rated Power		JUMPER Resistance Value	
				Z (±5%)	F (±1%)	Z (±5%)	F (±1%)
CPW05 (0201)	$\frac{1}{16}$ W	25V	50V	0.5A	0.5A	50mΩ MAX	35mΩ MAX
CPW10 (0402)	$\frac{1}{8}$ W	50V	100V	1.5A	2A	50mΩ MAX.	20mΩ MAX.
CPW16 (0603)	$\frac{1}{5}$ W	75V	150V	1.5A	2.5A	50mΩ MAX.	20mΩ MAX.
CPW21 (0805)	$\frac{1}{4}$ W	150V	300V	2.5A	3.5A	50mΩ MAX.	20mΩ MAX.
CPW32 (1206)	$\frac{1}{2}$ W	200V	400V	3A	5A	50mΩ MAX.	20mΩ MAX.
CPW40 (1210)	$\frac{3}{4}$ W	200V	400V	4A	6A	50mΩ MAX.	20mΩ MAX.
CPW50 (2010)	1W	200V	400V	4.5A	7A	50mΩ MAX.	20mΩ MAX.
CPW63 (2512)	2W	200V	400V	6A	10A	50mΩ MAX.	20mΩ MAX.



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### 3.2 Power Derating Curve:

Type	CPW05 (0201)	Other
Operating Temperature Range	-55°C ~ +125°C	-55°C ~ +155°C
Explain	If the ambient temperature exceeds 70 degrees centigrade to 125 degrees centigrade, the power can be modified by the curve as below.	If the ambient temperature exceeds 70 degrees centigrade to 125 degrees centigrade, the power can be modified by the curve as below.
Figure		

### 3.3 Standard Atmospheric Condition

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient Temperature = + 5°C to +35°C

Relative Humidity = < 85% RH

Air Pressure = 86 kPa to 106kPa

If there may be any doubt about the results, measurement shall be made within the following limits:

Ambient Temperature = 20 ± 2°C

Relative Humidity = 60 to 70% RH

Air Pressure = 86 kPa to 106kPa

3.4 Operating Temperature Range -55°C to +155°C (CPW05 : -55°C to +125°C)

3.5 Storage Temperature Range -5°C to + 40°C / < 85% RH

3.6 Flammability Rating Tested in accordance to UL-94, V-0

3.7 Moisture Sensitivity Level Rating: Level 1

3.8 Product Assurance  
ASJ resistor shall warranty 24 months from manufacturing date with control conditions.



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3.9 ASJ resistors are RoHS-compliant in accordance to RoHS Directive.

3.10 Resistance, Resistance Tolerance and Temperature Coefficient of Resistance.

Type	Rated Power at 70°C	Max. Working Voltage	Max. Overload Voltage	T.C.R (ppm/°C)	Resistance Range				JUMPER Rated Power		JUMPER Resistance Value	
					B(±0.1%) E-24、E-96	D(±0.5%) E-24、E-96	F(±1%) E-24、E-96	J(±5%) E-24	Z (±5%)	F (±1%)	Z (±5%)	F (±1%)
CPW05 (0201)	1/16 W	25V	50V	-200 +400	-----	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω	0.5A	0.5A	50mΩ MAX.	35mΩ MAX.
				±200	-----	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ				
CPW10 (0402)	1/8 W	50V	100V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 20MΩ	1.5A	2A	50mΩ MAX.	20mΩ MAX.
				±200	-----	-----	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω				
CPW16 (0603)	1/5 W	75V	150V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 20MΩ	1.5A	2.5A	50mΩ MAX.	20mΩ MAX.
				±200	-----	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω				
CPW21 (0805)	1/4 W	150V	300V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 20MΩ	2.5A	3.5A	50mΩ MAX.	20mΩ MAX.
				±200	-----	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω				
CPW32 (1206)	1/2 W	200V	400V	±100	10Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 20MΩ	3A	5A	50mΩ MAX.	20mΩ MAX.
				±200	3Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω				
CPW40 (1210)	3/4 W	200V	400V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 20MΩ	4A	6A	50mΩ MAX.	20mΩ MAX.
				±200	-----	-----	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω				
CPW50 (2010)	1W	200V	400V	±100	-----	-----	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ	4.5A	7A	50mΩ MAX.	20mΩ MAX.
				±200	-----	-----	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω				
CPW63 (2512)	2W	200V	400V	±100	100Ω ≤ R ≤ 100K	100Ω ≤ R ≤ 100K	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ	6A	10A	50mΩ MAX.	20mΩ MAX.
				±200	-----	-----	1Ω ≤ R < 10Ω	1Ω ≤ R < 10Ω				
Operating Temperature Range				-55°C ~ +155°C (0201: -55°C ~ +125°C)								

3.11 Rated Voltage

The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following :

$$E = \sqrt{R \times P}$$

E= Voltage rating (v)  
P= Power rating (w)  
R= Nominal resistance(Ω)



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## 4. MARKING ON PRODUCT

The nominal resistance shall be marked on the surface of each resistor

Part Number	Color	Marking on Product
CPW05 (0201)	-	No marking
CPW10 (0402)	-	No marking
CPW16 (0603)	Light Yellow	1) Tolerance : $\pm 0.1\%$ (B), $\pm 0.5\%$ (D), $\pm 1.0\%$ (F) ° Four Numerals Marking (E96 Series) ° 0603 Three Characters Marking based on E-96 marking standard. 2) Tolerance; $\pm 5.0\%$ (J) Three Numerals Marking 3) Zero ohm jumper resistor The marking used shall be 0
CPW21 (0805)	Light Yellow	
CPW32 (1206)	Light Yellow	
CPW40 (1210)	Light Yellow	
CPW50 (2010)	Light Yellow	
CPW63 (2512)	Light Yellow	

### 4.1 Numeric Numbering

#### 4.1.1 0.1%, 0.5%, 1% Tolerance : **Four Numerals Marking**

First 3 digits are significant figures; fourth digit is number of zeros.

Examples:

Nominal Resistance	Marking	Remarks
1 $\Omega$	1R00	$1 \times 10^0 = 1$
10 $\Omega$	10R0	$10 \times 10^0 = 10$
100 $\Omega$	1000	$100 \times 10^0 = 100$
4.7K $\Omega$	4701	$470 \times 10^1 = 4700$
47K $\Omega$	4702	$470 \times 10^2 = 47000$
470K $\Omega$	4703	$470 \times 10^3 = 470000$
1M $\Omega$	1004	$100 \times 10^4 = 1000000$

#### 4.1.2 5% Tolerance: **Three Numerals Marking**

First 2 digits are significant figures; third digit is number of zeros. Letter R is decimal point.

Example

Nominal Resistance	Marking	Remarks
1 $\Omega$	1R0	$1 \times 10^0 = 1$
10 $\Omega$	100	$10 \times 10^0 = 10$
100 $\Omega$	101	$10 \times 10^1 = 100$
4.7K $\Omega$	472	$47 \times 10^2 = 4700$
47K $\Omega$	473	$47 \times 10^3 = 47000$
470K $\Omega$	474	$47 \times 10^4 = 470000$
4.7M $\Omega$	475	$47 \times 10^5 = 4700000$

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### 4.1.3 0603 1% Tolerance: **Three Character E-96 Marking Standard.**

The first 2 digits for the 3 digits E-96 part marking standard, (Refer Table 2 & 3). The third character is a letter multiplier:

Nominal resistance	Marking	Remark
33.2 Ω	51 X	332 X 10 <sup>-1</sup> Ω
150 Ω	18 A	150 X 10 <sup>0</sup> Ω
4.99K Ω	68 B	499 X 10 <sup>1</sup> Ω
1 0.2K Ω	02 C	102 X 10 <sup>2</sup> Ω
100K Ω	01 D	100 10 <sup>3</sup> Ω

### 4.1.4 EIA-96 Marking Scheme

Table 2 Significant figures

Significant Figures	Symbol	Significant Figures	Symbol	Significant Figures	Symbol	Significant Figures	Symbol
100	01	178	25	316	49	562	73
102	02	182	26	324	50	576	74
105	03	187	27	332	51	590	75
107	04	191	28	340	52	604	76
110	05	196	29	348	53	619	77
113	06	200	30	357	54	634	78
115	07	205	31	365	55	649	79
118	08	210	32	374	56	665	80
121	09	215	33	383	57	681	81
124	10	221	34	392	58	698	82
127	11	226	35	402	59	715	83
130	12	232	36	412	60	732	84
133	13	237	37	422	61	750	85
137	14	243	38	432	62	768	86
140	15	249	39	442	63	787	87
143	16	255	40	453	64	806	88
147	17	261	41	464	65	825	89
150	18	267	42	475	66	845	90
154	19	274	43	487	67	866	91
158	20	280	44	499	68	887	92
162	21	287	45	511	69	909	93
165	22	294	46	523	70	931	94
169	23	301	47	536	71	953	95
174	24	309	48	549	72	976	96

Table 3 Multiplier

Symbol	Multiplier	Symbol	Multiplier
A	10 <sup>0</sup>	G	10 <sup>6</sup>
B	10 <sup>1</sup>	H	10 <sup>7</sup>
C	10 <sup>2</sup>	X	10 <sup>-1</sup>
D	10 <sup>3</sup>	Y	10 <sup>-2</sup>
E	10 <sup>4</sup>		
F	10 <sup>5</sup>		



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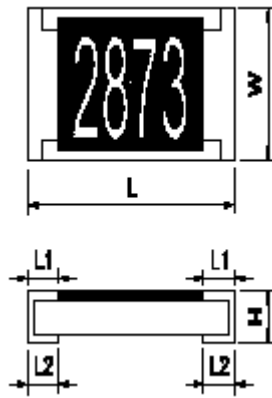
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## 5. DIMENSION, CONSTRUCTION AND MATERIAL

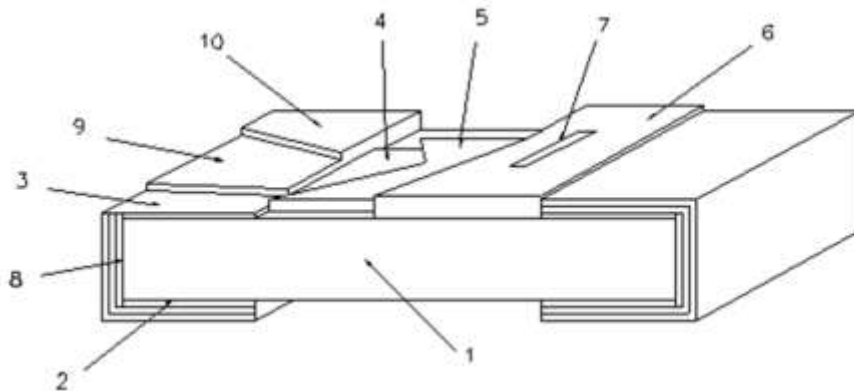
### 5.1 Dimension



Unit:mm

Dimension		L	W	H	L1	L2
Type	Size Code					
CPW05	0201	0.60±0.03	0.30±0.03	0.23±0.03	0.15±0.05	0.15±0.05
CPW10	0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
CPW16	0603	1.55±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
CPW21	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
CPW32	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
CPW40	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
CPW50	2010	4.95±0.10	2.45±0.10	0.70±0.10	0.65±0.20	0.60±0.20
CPW63	2512	6.40±0.20	3.20±0.20	0.70±0.10	0.60±0.20	1.25±0.20

### 5.2 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

### 5.3 Plating Thickness:

5.3.1 Ni :  $\geq 2 \mu\text{m}$

5.3.2 Sn (Tin) :  $\geq 3 \mu\text{m}$

5.3.3 Sn (Tin) : Matte Sn

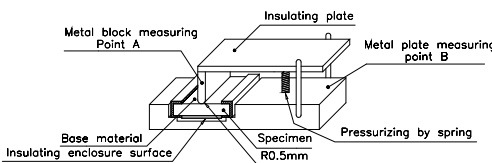


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## 6. RELIABILITY TEST

### 6.1 Electrical Performance Test

Item	Conditions	Specifications	
		Resistors	Jumper
Temperature Coefficient of Resistance	<p><b>Refer to JIS-C5201-1 4.8</b></p> $TCR(ppm/^{\circ}C) = \frac{(R2 - R1)}{R1(T2 - T1)} \times 10^6$ <p>R1: Resistance at room temperature                      R2: Resistance at -55°C or +125°C                      T1: Room temperature                      T2: Temperature -55°C or +125°C</p>	Refer to item 3.10	NA
Short Time Overload	<p><b>Refer to JIS-C5201-1 4.13</b></p> <p>Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications)</p>	0.1%、0.5%、1%: $\Delta R\% = \pm 1.0\%$ 5%: $\Delta R\% = \pm 2.0\%$	Refer to item 3.10
Insulation Resistance	<p><b>Refer to JIS-C5201-1 4.6</b></p> <p>Put the resistor in the fixture, add 100 VDC in +, - terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material.</p> 	$\geq 10^9 \Omega$	
Dielectric Withstand Voltage	<p><b>Refer to JIS-C5201-1 4.7</b></p> <p>Put the resistor in the fixture, add VAC (see spec. below) in +, - terminal for.                      CPW21、32、40、50、63 apply 500 VAC 1 minute.                      CPW10、16 apply 300 VAC 1 minute.</p>	No short or burned on the appearance.	



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## 6.2 Mechanical Performance Test

Item	Conditions	Specifications	
		Resistors	Jumper
Terminal Strength	<p><b>Refer to JIS-C5201-1 4.16</b></p> <p>Test1:The resistor mounted on the board applied 5N (CPW05:3N)pushing force on the sample rear for 10sec.</p> <p>Test2:The resistor mounted on the board slowly add force on the sample rear until the sample termination is breakdown.</p>	<p>Test1:No evidence of mechanical damage</p> <p>Test2:CPW05<math>\geq</math>3N Other type<math>\geq</math>5N</p>	
Resistance to Solvent	<p><b>Refer to JIS-C5201-1 4.29</b></p> <p>The tested resistor be immersed into isopropyl alcohol of 20~25°C for 5 minutes, then the resistor is left in the room for 48 hrs, and measured its resistance variance rate.</p>	<p>CPW05: <math>\Delta R\% = \pm 1.0\%</math> Other type: <math>\Delta R\% = \pm 0.5\%</math></p>	Refer to item 3.10
Solderability	<p><b>Refer to JIS-C5201-1 4.17</b></p> <p>Preconditioning: Put the tested resistor in the apparatus of PCT, at a temperature of 105°C, humidity of 100% RH, and pressure of <math>1.22 \times 10^5</math> Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more.</p> <p>Test method: The resistor be immersed into solder pot in temperature <math>235 \pm 5^\circ\text{C}</math> for 2 sec, then the resistor is left as placed under microscope to observed its solder area.</p>	Solder coverage over 95%	
Resistance to Soldering Heat	<p><b>Refer to JIS-C5201-1 4.18</b></p> <p>©Test method 1 (solder pot test): The tested resistor be immersed into molten solder of <math>260_{-0}^{+5}</math> °C for <math>10_{-0}^{+1}</math> seconds. Then the resistor is left in the room for 1 hour.</p> <p>©Test method 2 (solder pot test): The tested resistor be immersed into molten solder of <math>260_{-0}^{+5}</math> °C for <math>30_{-0}^{+1}</math> seconds. Then the resistor is left as placed under microscope to observe its solder area.</p> <p>©Test method 3 (Electric iron test): Preheating temperature : <math>350 \pm 10^\circ\text{C}</math> Electric iron preheating time : <math>3_{-0}^{+1}</math> sec Preheating the electric iron on electrode termination, as after that step placed the iron over 60 min. and measured its resistance variance rate.</p>	<p>Test item 1: (1).Variance rate on resistance <math>\Delta R\% = \pm 1.0\%</math></p> <p>Test item 2: (1).Solder coverage over 95%. (2).The underlying material (such as ceramic) shall not be visible at the crest corner area of the electrode.</p> <p>Test item 3: (1).Variance rate on resistance <math>\Delta R\% = \pm 1.0\%</math></p>	Refer to item 3.10



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Item	Conditions	Specifications	
		Resistors	Jumper
Joint Strength of Solder	<p><b>Refer to JIS-C5201-1 4.33</b></p> <p>©Bending Strength: Solder tested resistor on to PC board. Add force in the middle down, and under load measured its resistance variance rate.</p> <p>D:CPW10、16、21=5mm CPW05、32、40=3mm CPW50、63=2mm</p>	$\Delta R = \pm 1.0\%$	Refer to item 3.10



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## 6.3 Environmental Test

Item	Conditions	Specifications									
		Resistors	Jumper								
Resistance to Dry Heat	<b>Refer to JIS-C5201-1 4.25</b> Put tested resistor in chamber under temperature $155\pm 5^{\circ}\text{C}$ for $1000^{+48}_0$ hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.	0.1%、0.5%、1%: $\Delta R\% = \pm 1.0\%$ 5%: $\Delta R\% = \pm 2.0\%$	Refer to item 3.10								
Thermal Shock	<b>Refer to MIL-STD 202 Method 107</b> Put the tested resistor in the chamber under the Thermal Shock which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variance rate. <table border="1" data-bbox="323 763 919 913"> <thead> <tr> <th colspan="2">Testing Condition</th> </tr> </thead> <tbody> <tr> <td>Lowest Temperature</td> <td><math>-55\pm 5^{\circ}\text{C}</math></td> </tr> <tr> <td>Highest Temperature</td> <td><math>125\pm 5^{\circ}\text{C}</math></td> </tr> <tr> <td>Temperature-retaining time</td> <td>15 minutes each</td> </tr> </tbody> </table>	Testing Condition		Lowest Temperature	$-55\pm 5^{\circ}\text{C}$	Highest Temperature	$125\pm 5^{\circ}\text{C}$	Temperature-retaining time	15 minutes each	0.1%、0.5%、1%: $\Delta R\% = \pm 0.5\%$ 5%: $\Delta R\% = \pm 1.0\%$	Refer to item 3.10
Testing Condition											
Lowest Temperature	$-55\pm 5^{\circ}\text{C}$										
Highest Temperature	$125\pm 5^{\circ}\text{C}$										
Temperature-retaining time	15 minutes each										
Loading Life in Moisture	<b>Refer to JIS-C5201-1 4.24</b> Put the tested resistor in the chamber under temperature $40\pm 2^{\circ}\text{C}$ , relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.	0.1%、0.5%、1%: $\Delta R\% = \pm 0.5\%$ 5%: $\Delta R\% = \pm 2.0\%$	Refer to item 3.10								
Load Life	<b>Refer to JIS-C5201-1 4.25</b> Put the tested resistor in chamber under temperature $70\pm 2^{\circ}\text{C}$ and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.	0.1%、0.5%、1%: $\Delta R\% = \pm 0.5\%$ 5%: $\Delta R\% = \pm 2.0\%$	Refer to item 3.10								



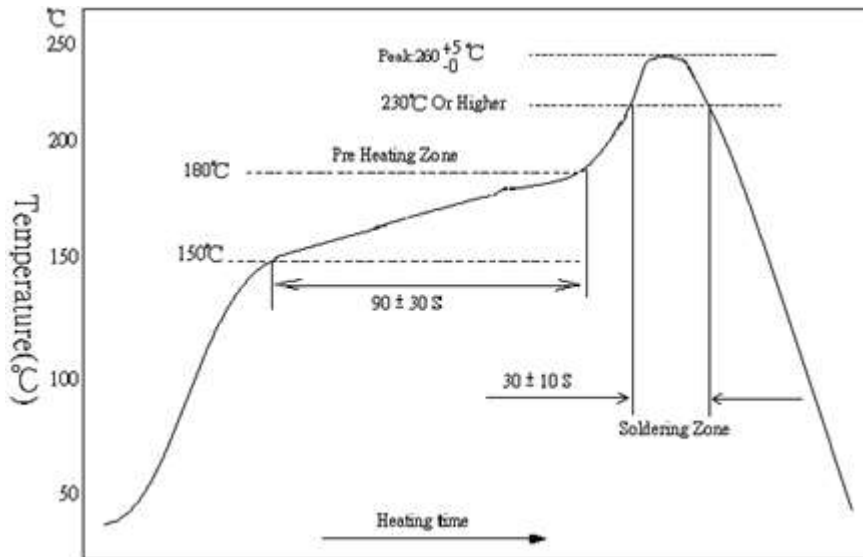
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6.4 Technical application notes: (This is for recommendation, customer please perform adjustment according to actual application)

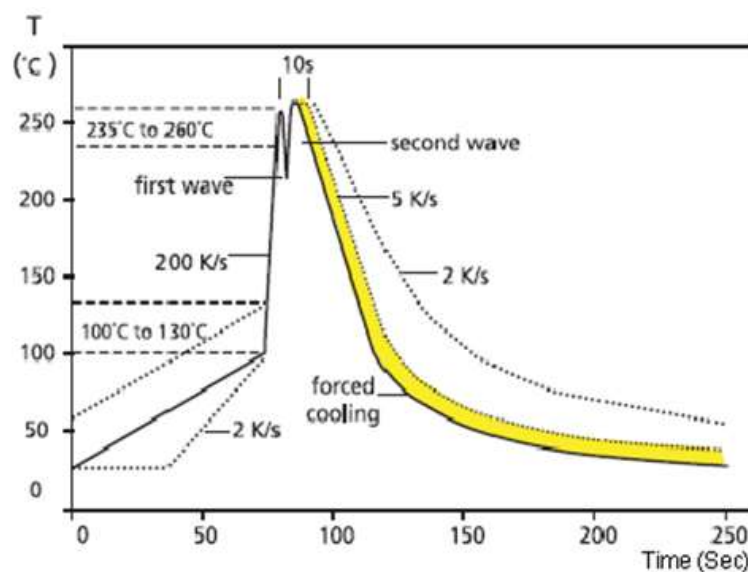
Soldering Profile:

### 6.1.1 Lead-Free IR Re-flow Soldering Profile



Remark : The peak temperature of soldering heat is  $260 \pm 5$  °C for 10 seconds.

### 6.1.2 Lead-Free Double Wave Soldering Profile (This applies to 0603 size inclusive above product)

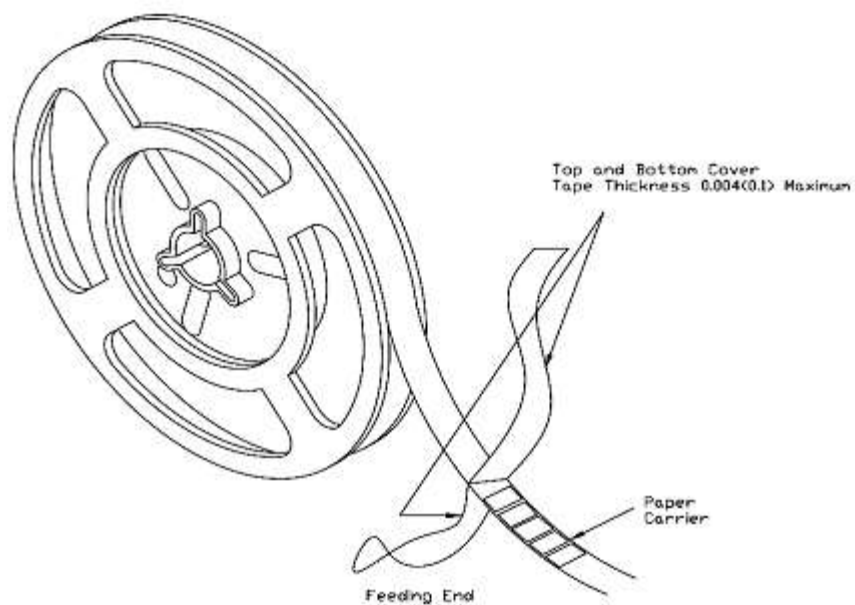


6.1.3 Soldering Iron: Temperature  $350 \pm 10$  °C , dwell time shall be less than 3 sec.

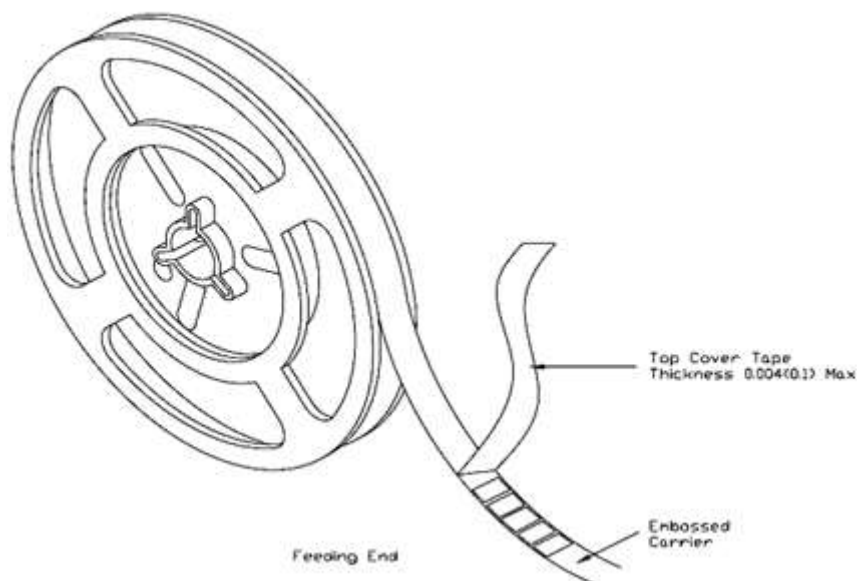
## 7. TAPING

### 7.1 Structure of Taping

Paper Carrier

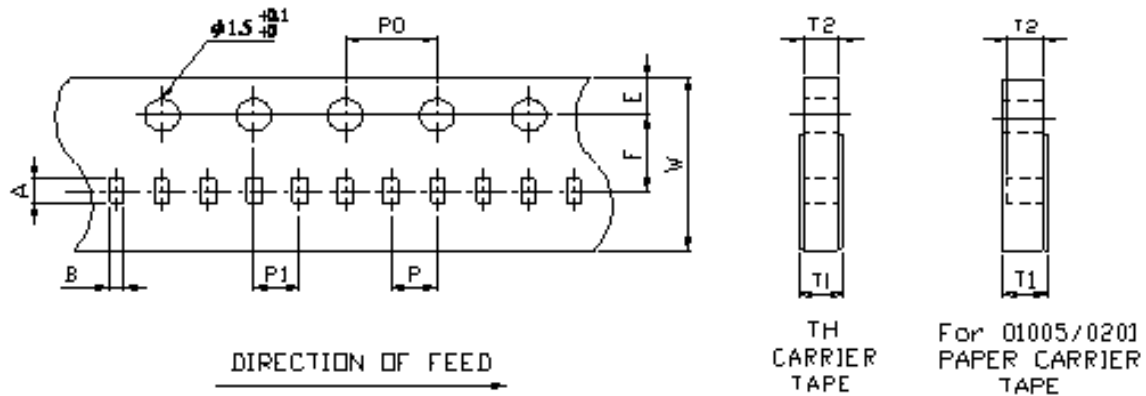


Embossed Plastic Carrier



## 7.2 Dimension

### 7.2.1 Dimension of Punched Paper Tape Carrier System (CPW – 05, 10)



Remark : Pitch tolerance over any 10 pitches of P<sub>0</sub> is ± 0.2 mm

### Dimension of Punched Paper Tape Carrier System (CPW- 05, 10)

Type	Dimensions (mm)										
Size	A	B	W	E	F	T1	T2	P	P0	10xP0	P1
CPW05	0.68±0.05	0.38±0.03	8.00±0.10	1.75±0.10	3.50±0.05	0.42 <sup>+0.1</sup> <sub>0</sub>	0.28±0.02	2.00±0.05	4.00±0.05	40.00±0.20	2.00±0.05
CPW10	1.15±0.05	0.65±0.05	8.00±0.20	1.75±0.10	3.50±0.05	0.40 <sup>+0.2</sup> <sub>0</sub>	0.40±0.05	2.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05

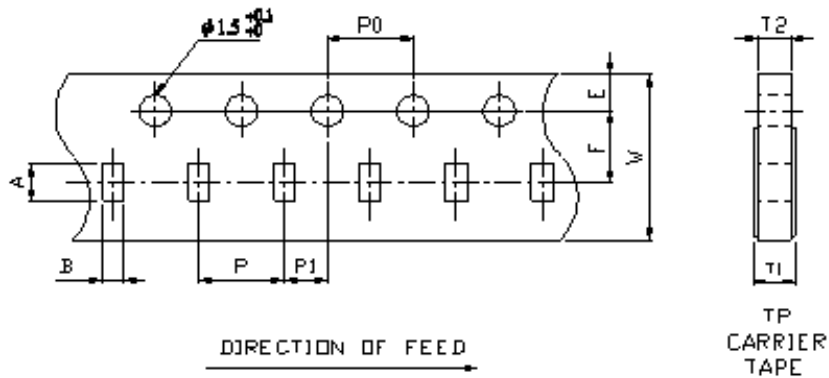
# HIGH POWER RATING CHIP RESISTOR

CPW Series

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## 7.2.2 Dimension of Punched Paper Tape Carrier System /Plastic Embossed Carrier System Carrier System (CPW-16, 21, 32, 40, 50, 63)

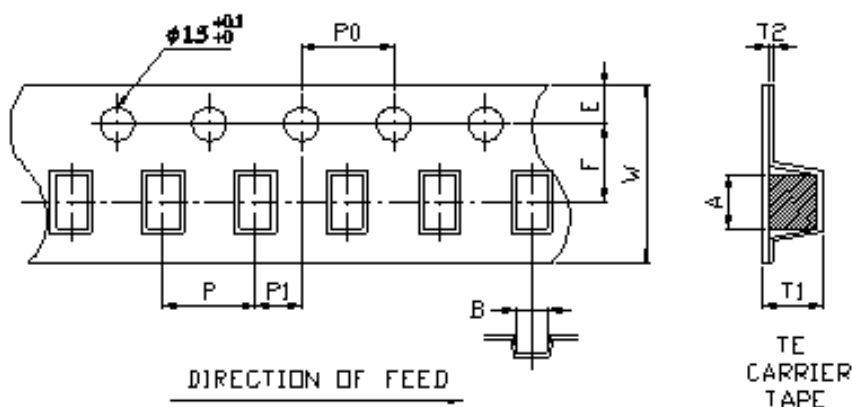


Remark : Pitch tolerance over any 10 pitches of  $P_0$  is  $\pm 0.2$  mm

### Dimension of Punched Paper Tape Carrier System (CPW - 16, 21, 32, 40)

Type	Dimensions (mm)										
Size	A	B	W	E	F	T1	T2	P	P0	10xP0	P1
CPW16	1.80±0.10	1.00±0.10	8.00±0.20	1.75±0.10	3.50±0.05	0.60 <sup>+0.2</sup> <sub>0</sub>	0.60±0.10	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05
CPW21	2.30±0.10	1.55±0.10	8.00±0.20	1.75±0.10	3.50±0.05	0.75 <sup>+0.2</sup> <sub>0</sub>	0.75±0.10	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05
CPW32	3.50±0.20	1.90±0.20	8.00±0.20	1.75±0.10	3.50±0.05	0.75 <sup>+0.2</sup> <sub>0</sub>	0.75±0.10	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05
CPW40	3.50±0.20	2.80±0.20	8.00±0.20	1.75±0.10	3.50±0.05	0.75 <sup>+0.2</sup> <sub>0</sub>	0.75±0.10	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05

### Dimension of Plastic Embossed Carrier System (CPW -50, 63)



Type	Dimensions (mm)										
Size	A	B	W	E	F	T1	T2	P	P0	10xP0	P1
CPW50	5.50±0.20	2.80±0.20	12.00±0.20	1.75±0.10	5.50±0.05	1.10±0.15	0.23±0.15	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05
CPW63	6.70±0.20	3.40±0.20	12.00±0.20	1.75±0.10	5.50±0.05	1.10±0.15	0.23±0.15	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05



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## 7.3 Packaging

### 7.3.1 Taping

#### Quantity – Tape and Reels

Code	Quantity	Reel	Remark
CPW05 CPW10	10,000 pcs	7"	2mm pitch
	20,000 pcs	7"	2mm pitch
	50,000 pcs	13"	2mm pitch
CPW16 CPW21 CPW32 CPW40	5,000 pcs	7"	4mm pitch
	10,000 pcs	10"	
	20,000 pcs	13"	
CPW50	4,000 pcs	7"	Embossed 4mm pitch
CPW63	4,000 pcs	7"	Embossed 4mm pitch

### 7.3.2 Identification

Production label that indicates the 10 digits lot number, product type, resistance value and tolerance shall be pasted on the surface of each reel.

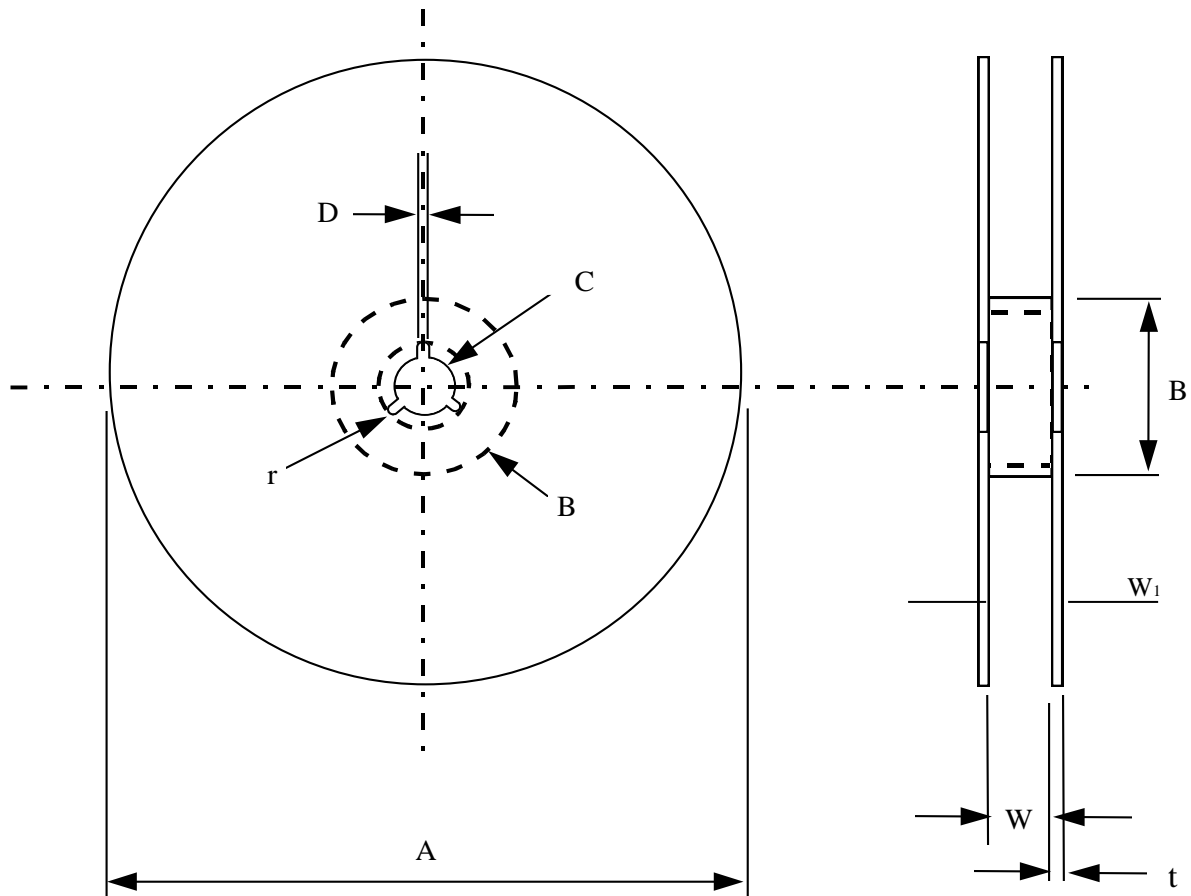


### 7.3.3 Packaging Reel Box

Dimension	Reel Box	Number of Reels
185 × 60 × 186 mm	25K Box	5
185 × 120 × 186 mm	50K Box	10



## 7.3.4 Reel Dimension

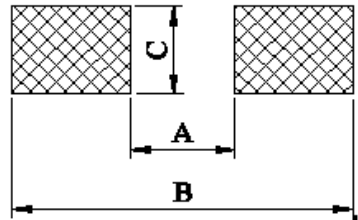


Model	A	B	C	D	W	W <sub>1</sub>	t	r
7" Reel (5K) (except 0402 10K)	φ178±2.0	φ80min	13±0.2	φ2.0±0.5	11±0.1	14.4 max	1.0±0.1	1.0
7" Reel (4K)	φ178±2.0	φ60min	13±0.2	φ2.0±0.5	13±1.0	14.4 max	1.2±0.1	1.0
7" Reel (2K) (for 2512)	φ178±2.0	φ60min	13.5±0.5	φ2.0±0.5	13.8±0.5	14.4 max	1.2±0.1	1.0
10" Reel (10K)	φ254±2.0	φ60min	13±0.2	φ2.0±0.5	11±1.0	14.4 max	1.5±0.1	1.0
13" Reel (20K)	φ330±2.0	φ60min	13±0.2	φ2.0±0.5	11±1.0	14.4 max	2.1±0.1	-
13" Reel (20K)	φ330±1.0	φ100±1	13.5±0.5	2~3±0.5	10±0.5	-	-	-

## 8 SURFACE MOUNT LAND PATTERNS DESIGN (FOR REFLOW SOLDERING)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

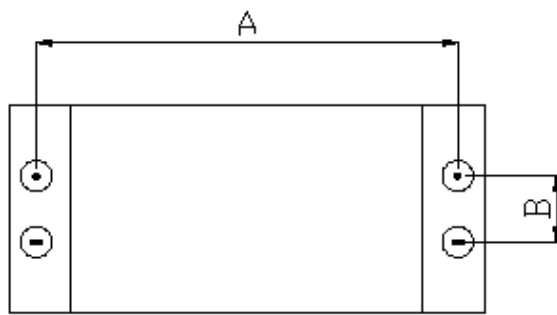
Unit:mm



TYPE	DIM		
	A	B	C
CPW05	0.3	1.0	0.4
CPW10	0.5	1.5	0.6
CPW16	0.8	2.1	0.9
CPW21	1.2	3.0	1.3
CPW32	2.2	4.2	1.6
CPW40	2.2	4.2	2.8
CPW50	3.5	6.1	2.8
CPW63	3.8	8.0	3.5

## 9 MEASUREMENT POINT

Bottom electrode		Unit : mm	
TYPE	DIM		B
	A		
CPW05	0.44±0.05	0.22±0.05	
CPW10	0.80±0.05	0.24±0.05	
CPW16	1.35±0.05	0.35±0.05	
CPW21	1.80±0.05	0.35±0.05	
CPW32	2.90±0.05	0.35±0.05	
CPW40	2.90±0.05	0.35±0.05	
CPW50	4.50±0.05	1.15±0.05	
CPW63	5.90±0.05	1.60±0.05	



⊕ **Current Terminal**  
 ⊖ **Voltage Terminal**

# HIGH POWER RATING CHIP RESISTOR

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## 10 REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version.1	13.02.2015		Initial Release
Version.2	15.12.2015		Surface Mount Land Pattern updated
Version.3	23.05.2016	ECO No.: 002/2016	Revise clause 2 and 7.7.1.1, change CPW63 packaging from 2000 pcs to 4000 pcs , Revise clause 5.1, change Chip dimension CPW63 (H) from 1.05±0.1 to 0.70±0.10,
Version.4	28.10.2016	PCN-ECO: 01/2016	Update clause 2, Part Numbering System Update clause 3.10, Resistance, Resistance Tolerance and TCR information Update clause 7.7.4, 13" reel information
Version.5	24.10.2017		Typo error on clause 2 Typo error on 3.10
Version.6	21.06.2018		Remove AEC-Q200 standard on clause 1.3
Version.7	28.12.2018		Update Datasheet
Version.8	12.04.2019		Update Datasheet
Version.9	28.08.2019		Update clause 3.9
Version.10	24.06.2020		Revise clause 2 part numbering system
Version.11	05.08.2020		Add clause 1.3
Version.12	02.11.2020		Revise clause 3.1.1 Resistor rated power Revise clause 3.10 TCR table
Version.13	15.09.2021		Revise clause 3.1.1 resistor rated power table Revise clause 3.10 TCR table
Version 14	28.04.2023		Revise clause 3.8 Product Assurance
Version 15	02.04.2024		Revise clause 7.3.2 Identification



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# DATA SHEET

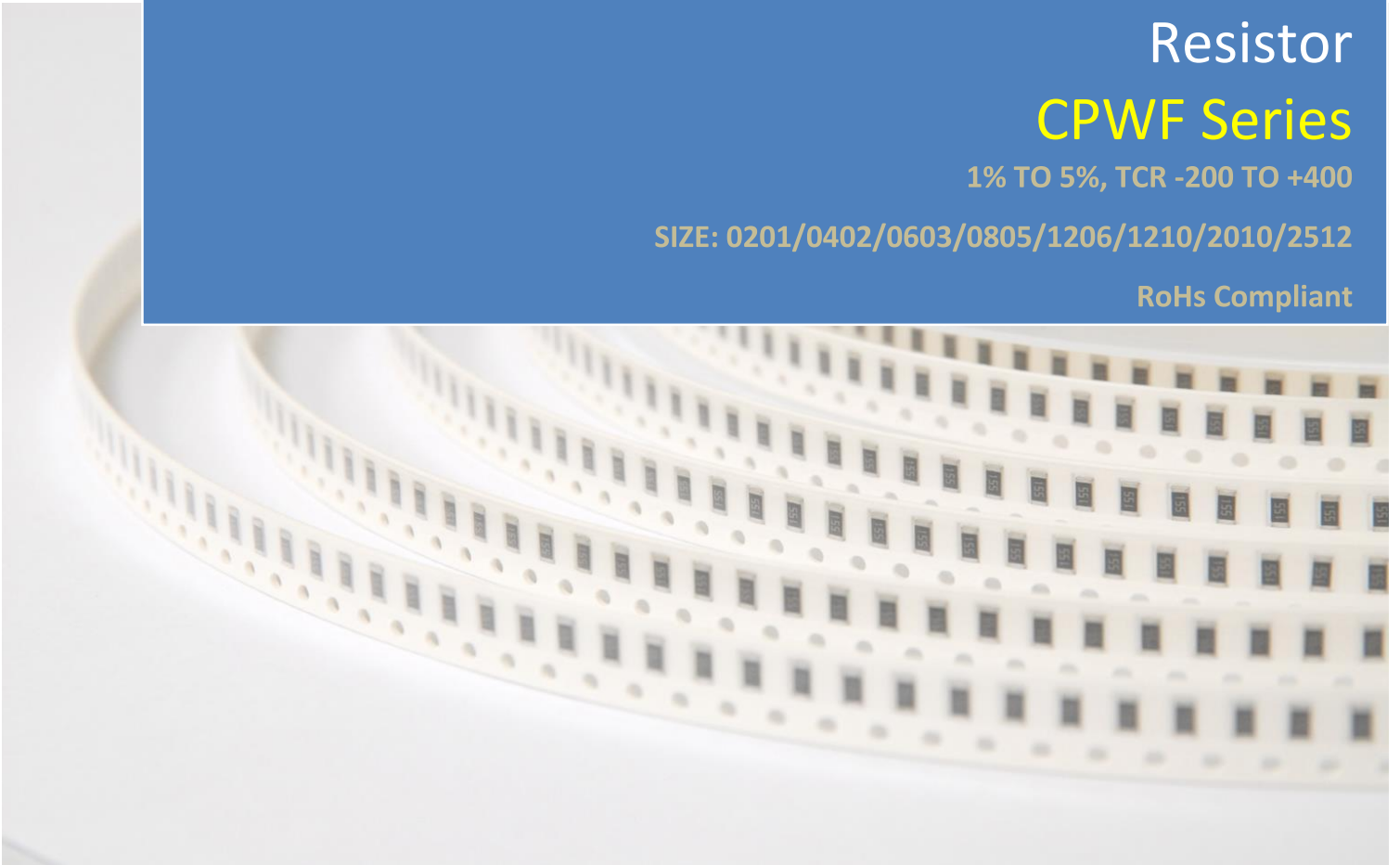
## Fully Lead-Free High Power Thick Film Chip Resistor

### CPWF Series

1% TO 5%, TCR -200 TO +400

SIZE: 0201/0402/0603/0805/1206/1210/2010/2512

RoHs Compliant



# FULLY LEAD-FREE HIGH POWER THICK FILM CHIP RESISTORS

CPWF Series

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## 1. SCOPE

- 1.1 This specification is applicable to CPWF series fully lead-free and halogen-free high power thick film chip resistors.
- 1.2 Fully lead-free products – No RoHS exemptions.
- 1.3 The product is for general electronic purpose.

## 2. PART NUMBERING SYSTEM

Part Numbering is made in accordance with the following system:

CPWF	32	-	100		-	J	L
Type	Size	Nominal Resistance		Resistance Tolerance	Packaging		
Fully Lead-Free High Power Thick Film Chip Resistors	05(0201)	Resistors	5% (3-Digit)	EX. 10Ω=100 4.7Ω=4R7 Jumper=000	F=±1% J=±5% Z=Zero ohm	E = 4,000 pcs Lead Free L = 5,000 pcs Lead Free K = 10,000 pcs Lead Free Y = 20,000 pcs Lead Free  Refer to clause 7.3.1.1	
	10(0402)		1% (4-Digit)	EX. 10.2Ω=10R2 10KΩ=1002 Jumper=0000			
	16(0603)						
	21(0805)						
	32(1206)						
	40(1210)						
	50(2010)						
	63(2512)						

## 3. RATING

### 3.1 Rated Power

#### 3.1.1 Resistor Rated Power

Type	Rate Power at 70°C	Max. Working Current	Max. Overload Current
CPWF05 (0201)	$\frac{1}{16}$ W	25V	50V
CPWF10 (0402)	$\frac{1}{8}$ W	50V	100V
CPWF16 (0603)	$\frac{1}{5}$ W	75V	150V
CPWF21 (0805)	$\frac{1}{4}$ W	150V	300V
CPWF32 (1206)	$\frac{1}{2}$ W	200V	400V
CPWF40 (1210)	$\frac{3}{4}$ W	200V	400V
CPWF50 (2010)	1W	200V	400V
CPWF63 (2512)	1.5W	200V	400V



Product Specification

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# FULLY LEAD-FREE HIGH POWER THICK FILM CHIP RESISTORS

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### 3.2 Power Derating Curve:

Type	CPWF05 (0201)	Other
Operating Temperature Range	- 55°C ~ + 125°C	- 55°C ~ + 155°C
Explain	For resistors operated in ambient temperatures above 70°C, power rating shall be derated in accordance with figure below.	For resistors operated in ambient temperatures above 70°C, power rating shall be derated in accordance with figure below.
Figure	<p>A line graph with 'Rating Power (%)' on the y-axis (0 to 100) and 'Ambient Temperature (°C)' on the x-axis (-55 to 160). The curve is horizontal at 100% from -55°C to 70°C, then slopes downward to 0% at 125°C. Dashed vertical lines mark 70 and 125 on the x-axis.</p>	<p>A line graph with 'Rating Power (%)' on the y-axis (0 to 100) and 'Ambient Temperature (°C)' on the x-axis (-55 to 160). The curve is horizontal at 100% from -55°C to 70°C, then slopes downward to 0% at 155°C. Dashed vertical lines mark 70 and 155 on the x-axis.</p>

### 3.3 Standard Atmospheric Condition

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient Temperature = + 5°C to +35°C

Relative Humidity = < 85% RH

Air Pressure = 86 kPa to 106kPa

If there may be any doubt about the results, measurement shall be made within the following limits:

Ambient Temperature = 20 ± 2°C

Relative Humidity = 60 to 70% RH

Air Pressure = 86 kPa to 106kPa

3.4 Operating Temperature Range -55°C to +155°C

3.5 Storage Temperature Range -5°C to + 40°C / < 85% RH

3.6 Flammability Rating Tested in accordance to UL-94, V-0

3.7 Moisture Sensitivity Level Rating: Level 1

3.8 Product Assurance ASJ resistors shall warranty 24 months from manufacturing date with control condition.

3.9 ASJ resistors are RoHS compliance in accordance to RoHS Directive.



Product Specification

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3.10 Resistance, Resistance Tolerance and Temperature Coefficient of Resistance.

Type	Rate Power at 70°C	Max. Working Current	Max. Overload Current	T.C.R (ppm/°C)	Resistance Range		JUMPER Rated Current		JUMPER Resistance Value	
					F(±1%) E-24, E-96	J(±5%) E-24	Z (±5%)	F (±1%)	Z (±5%)	F (±1%)
CPWF05 (0201)	$\frac{1}{16}$ W	25V	50V	-200 / +400	$1\Omega \leq R \leq 2.2M\Omega$	$1\Omega \leq R \leq 2.2M\Omega$	0.5A	0.5A	50mΩ MAX	30mΩ MAX
				±200	$2.2M\Omega < R \leq 10M\Omega$	$2.2M\Omega < R \leq 10M\Omega$				
CPWF10 (0402)	$\frac{1}{8}$ W	50V	100V	±150	$1\Omega \leq R \leq 2.2M\Omega$	$1\Omega \leq R \leq 2.2M\Omega$	1.5A	2A	50mΩ MAX	20mΩ MAX
				±250	$2.2M\Omega < R \leq 10M\Omega$	$2.2M\Omega < R \leq 10M\Omega$				
CPWF16 (0603)	$\frac{1}{5}$ W	75V	150V	±150	$1\Omega \leq R \leq 2.2M\Omega$	$1\Omega \leq R \leq 2.2M\Omega$	1.5A	2.5A	50mΩ MAX	20mΩ MAX
				±250	$2.2M\Omega < R \leq 10M\Omega$	$2.2M\Omega < R \leq 10M\Omega$				
CPWF21 (0805)	$\frac{1}{4}$ W	150V	300V	±150	$1\Omega \leq R \leq 2.2M\Omega$	$1\Omega \leq R \leq 2.2M\Omega$	2.5A	3.5A	50mΩ MAX	20mΩ MAX
				±250	$2.2M\Omega < R \leq 10M\Omega$	$2.2M\Omega < R \leq 10M\Omega$				
CPWF32 (1206)	$\frac{1}{2}$ W	200V	400V	±150	$1\Omega \leq R \leq 2.2M\Omega$	$1\Omega \leq R \leq 2.2M\Omega$	3A	5A	50mΩ MAX	20mΩ MAX
				±250	$2.2M\Omega < R \leq 10M\Omega$	$2.2M\Omega < R \leq 10M\Omega$				
CPWF40 (1210)	$\frac{3}{4}$ W	200V	400V	±150	$1\Omega \leq R \leq 2.2M\Omega$	$1\Omega \leq R \leq 2.2M\Omega$	4A	6A	50mΩ MAX	20mΩ MAX
				±250	$2.2M\Omega < R \leq 10M\Omega$	$2.2M\Omega < R \leq 10M\Omega$				
CPWF50 (2010)	1W	200V	400V	±150	$1\Omega \leq R \leq 2.2M\Omega$	$1\Omega \leq R \leq 2.2M\Omega$	4.5A	7A	50mΩ MAX	20mΩ MAX
				±250	$2.2M\Omega < R \leq 10M\Omega$	$2.2M\Omega < R \leq 10M\Omega$				
CPWF63 (2512)	1.5W	200V	400V	±150	$1\Omega \leq R \leq 2.2M\Omega$	$1\Omega \leq R \leq 2.2M\Omega$	6A	10A	50mΩ MAX	20mΩ MAX
				±250	$2.2M\Omega < R \leq 10M\Omega$	$2.2M\Omega < R \leq 10M\Omega$				
Operating Temperature Range				-55°C ~ +155°C(0201:-55°C ~ +125°C)						

3.11 Rated Voltage:

The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following

$$E = \sqrt{R \times P}$$

E= Voltage rating (v)  
P= Power rating (w)  
R= Nominal resistance(Ω)



4. MARKING ON PRODUCT

The nominal resistance shall be marked on the surface of each resistor

Type		Resistance Range	Tolerance $\leq 1\%$	Tolerance $> 1\%$
Single	Sizes: 0201、 0402	All	No Marking	
		Jumper=0Ω		
	Size: 0603	< 1Ω	4-digits Marking	4-digits Marking
		$\geq 1\Omega$	3-digits Marking	3-digits Marking
		Jumper=0Ω	3-digits Marking	1-digit Marking
	Sizes: 0805、 1206、 1210 1812、 2010、 2512	< 1Ω	4-digits Marking	4-digits Marking
		$\geq 1\Omega$	4-digits Marking	3-digits Marking
		Jumper=0Ω	3-digits Marking	1-digit Marking

4.1 Numeric Numbering

4.1.1 1% Tolerance : **Four Numerals Marking**

First 3 digits are significant figures; fourth digit is number of zeros.

Examples:

Nominal Resistance	Marking	Remarks
1 Ω	1R00	$1 \times 10^0 = 1$
10 Ω	10R0	$10 \times 10^0 = 10$
100 Ω	1000	$100 \times 10^0 = 100$
4.7K Ω	4701	$470 \times 10^1 = 4700$
47K Ω	4702	$470 \times 10^2 = 47000$
470K Ω	4703	$470 \times 10^3 = 470000$
1M Ω	1004	$100 \times 10^4 = 1000000$

4.1.2 5% Tolerance: **Three Numerals Marking**

First 2 digits are significant figures; third digit is number of zeros. Letter R is decimal point.

Example

Nominal Resistance	Marking	Remarks
1 Ω	1R0	$1 \times 10^0 = 1$
10 Ω	100	$10 \times 10^0 = 10$
100 Ω	101	$10 \times 10^1 = 100$
4.7K Ω	472	$47 \times 10^2 = 4700$
47K Ω	473	$47 \times 10^3 = 47000$
470K Ω	474	$47 \times 10^4 = 470000$
4.7M Ω	475	$47 \times 10^5 = 4700000$





# FULLY LEAD-FREE HIGH POWER THICK FILM CHIP RESISTORS

CPWF Series

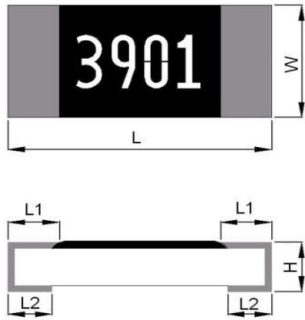
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## 5. DIMENSIONS, CONSTRUCTIONS AND MATERIALS

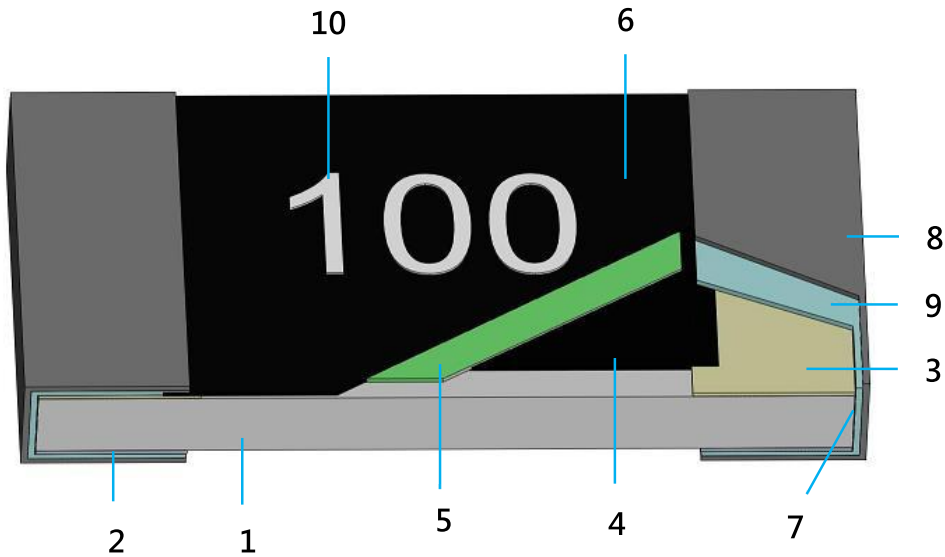
### 5.1 Dimensions

Unit:mm



Dimension		L	W	H	L1	L2
Type	Size Code					
CPWF05	0201	0.60±0.03	0.30±0.03	0.23±0.03	0.10±0.05	0.15±0.05
CPWF10	0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
CPWF16	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
CPWF21	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
CPWF32	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
CPWF40	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
CPWF50	2010	4.95±0.10	2.45±0.20	0.70±0.10	0.65±0.20	0.60±0.20
CPWF63	2512	6.40±0.20	3.20±0.20	0.70±0.10	0.60±0.20	1.25±0.20

### 5.2 Structure Graph:



1	Ceramic substrate	6	2 <sup>nd</sup> Protective coating
2	Bottom inner electrode	7	Terminal inner electrode
3	Top inner electrode	8	Sn plating
4	Resistive layer	9	Ni plating
5	1 <sup>st</sup> Protective coating	10	MK layer

### 5.3 Plating Thickness:

5.3.1 Ni: ≥2μm

5.3.2 Sn(Tin): ≥3μm

5.3.3 Sn(Tin): Matte Sn



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# FULLY LEAD-FREE HIGH POWER THICK FILM CHIP RESISTORS

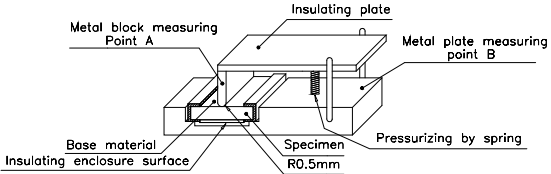
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## 6 Reliability Test

### 6.0.1 Electrical Performance Test

Item	Conditions	Specifications																											
		Resistors																											
Temperature Coefficient of Resistance	$TCR(ppm/^{\circ}C) = \frac{(R2 - R1)}{R1(T2 - T1)} \times 10^6$ <p>R1: Resistance at room temperature                      R2: Resistance at -55°C or +125°C                      T1: Room temperature                      T2: Temperature -55°C or +125°C</p> Refer to JIS-C5201-1 4.8	Refer to item 3.10																											
Short Time Overload	Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3.10)  Refer to JIS-C5201-1 4.13	1%: $\Delta R = \pm 1.0\%$ 5%: $\Delta R = \pm 2.0\%$																											
Insulation Resistance	Put the resistor in the fixture, add 100 VDC in +, - terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material.  Refer to JIS-C5201-1 4.6  	$\geq 10^9 \Omega$																											
Dielectric Withstand Voltage	Put the resistor in the fixture, add VAC (see SPEC below) in +, - terminal for.  CPWF05, 10, 16 apply 300VAC 1 minute CPWF21, 32, 40, 50, 63 apply 500 VAC 1 minute.  Refer to JIS-C5201-1 4.7	No short or burned on the appearance.																											
Intermittent Overload	Put the tested resistor in chamber under temperature $25 \pm 2^{\circ}C$ and load 2.5 times rated DC voltage for 1 sec on, 25 sec off, $10,000_{-0}^{+400}$ test cycles, then it be left at no-load for 1 hour, then measure its resistance variance rate. Jumper: Applied maximum overload current <table border="1" data-bbox="344 1549 959 1654"> <thead> <tr> <th>Size Jumper</th> <th>(0201)</th> <th>(0402)</th> <th>(0603)</th> <th>(0805)</th> <th>(1206)</th> <th>(1210)</th> <th>(2010)</th> <th>(2512)</th> </tr> </thead> <tbody> <tr> <td><math>\pm 5\%</math></td> <td>1.25A</td> <td>3.75A</td> <td>3.75A</td> <td>6.25A</td> <td>7.5A</td> <td>10A</td> <td>11.25A</td> <td>15A</td> </tr> <tr> <td><math>\pm 1\%</math></td> <td>1.25A</td> <td>5A</td> <td>6.25A</td> <td>8.75A</td> <td>12.5A</td> <td>15A</td> <td>17.5A</td> <td>25A</td> </tr> </tbody> </table> Refer to JIS-C5201-1 4.13	Size Jumper	(0201)	(0402)	(0603)	(0805)	(1206)	(1210)	(2010)	(2512)	$\pm 5\%$	1.25A	3.75A	3.75A	6.25A	7.5A	10A	11.25A	15A	$\pm 1\%$	1.25A	5A	6.25A	8.75A	12.5A	15A	17.5A	25A	$\Delta R = \pm 5.0\%$
Size Jumper	(0201)	(0402)	(0603)	(0805)	(1206)	(1210)	(2010)	(2512)																					
$\pm 5\%$	1.25A	3.75A	3.75A	6.25A	7.5A	10A	11.25A	15A																					
$\pm 1\%$	1.25A	5A	6.25A	8.75A	12.5A	15A	17.5A	25A																					



Product Specification

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**6.0.2 Mechanical Performance Test**

Item	Conditions	Specifications
		Resistors
Terminal Strength	Test 1 : The resistor mounted on the board applied 5N pushing force on the sample rear for 10 sec. Test 2 : The resistor mounted on the board slowly add force on the sample rear until the sample termination is breakdown.  Refer to JIS-C5201-1 4.16	Test 1 : No evidence of mechanical damage. Test 2: CPWF05 $\geq$ 3N OtypeType $\geq$ 5N
Resistance to Solvent	The tested resistor be immersed into isopropyl alcohol of 20~25°C for 5 minutes, then the resistor is left in the room for 48 hrs, and measured its resistance variance rate.  Refer to JIS-C5201-1 4.29	$\Delta R\% = \pm 1.0\%$
Solderability	Preconditioning Put the tested resistor in the apparatus of PCT, at a temperature of 105°C, humidity of 100% RH, and pressure of $1.22 \times 10^5$ Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more. Test method: The resistor be immersed into solder pot in temperature $235 \pm 5^\circ\text{C}$ for 2 sec, then the resistor is left as placed under microscope to observed its solder area.  Refer to JIS-C5201-1 4.17	Solder coverage over 95%
Resistance to Soldering Heat	©Test method 1 (Solder pot test): The tested resistor be immersed into molten solder of $260^{+5}_{-0}^\circ\text{C}$ for 10 seconds. Then the resistor is left in the room for 1 hour.  ©Test method 2 (Solder pot test): The tested resistor be immersed into molten solder of $260^{+5}_{-0}^\circ\text{C}$ for 30 seconds. Then the resistor is left as placed under microscope to observe its solder area.  ©Test method 3 (Electric iron test): Preheating temperature : $350 \pm 10^\circ\text{C}$ Electric iron preheating time : $3^{+1}_0$ sec Preheating the electric iron on electrode termination, as after that step placed the iron over 60 min. and measured its resistance variance rate.  Refer to JIS-C5201-1 4.18	Test item 1: (1).Variance rate on resistance $\Delta R\% = \pm 1.0\%$  Test item 2: (1).Solder coverage over 95%. (2).The underlying material (such as ceramic) shall not be visible at the crest corner area of the electrode.  Test item 3: (1).Variance rate on resistance $\Delta R\% = \pm 1.0\%$

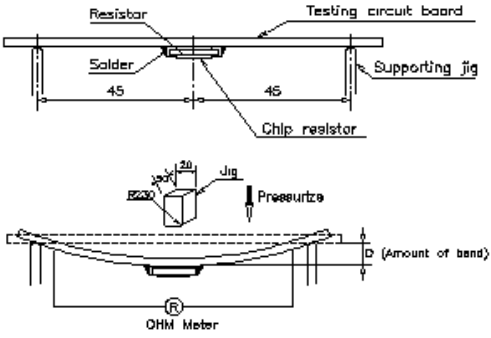


# FULLY LEAD-FREE HIGH POWER THICK FILM CHIP RESISTORS

CPWF Series

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Item	Conditions	Specifications
Joint Strength of Solder	<p>©Bending Strength                      Solder tested resistor on to PC board add force in the middle down, and under load measured its resistance variance rate.                      D: CPWF10, 16, 21=5mm                      CPWF05, 32, 40=3mm                      CPWF50、63=2mm</p>  <p>Refer to JIS-C5201-1 4.33</p>	<p><math>\Delta R = \pm 1.0\%</math></p>



Product Specification

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**6.0.3 Environmental Test**

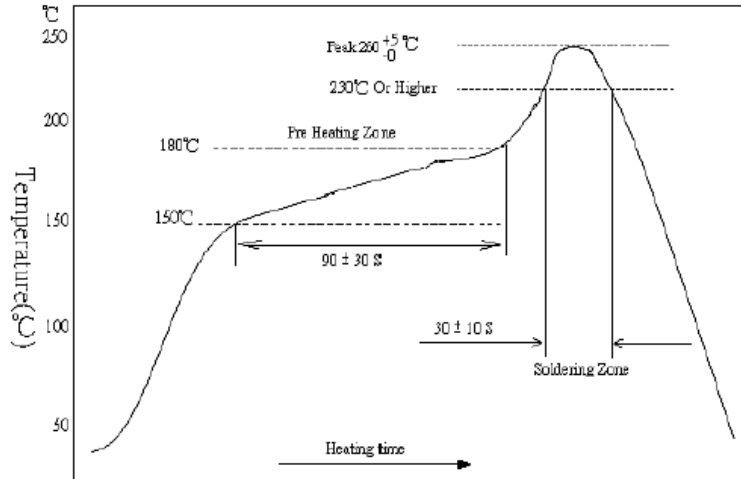
Item	Conditions	Specifications								
		Resistors								
Resistance to Dry Heat	Put tested resistor in chamber under temperature $155\pm 5^{\circ}\text{C}$ for $1,000_{-0}^{+48}$ hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.  Refer to JIS-C5201-1 4.25	1%: $\Delta R\%=\pm 1.0\%$ 5%: $\Delta R\%=\pm 2.0\%$								
Thermal Shock	Put the tested resistor in the chamber under the Thermal Shock which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variance rate. <table border="1" data-bbox="370 655 919 787"> <thead> <tr> <th colspan="2">Testing Condition</th> </tr> </thead> <tbody> <tr> <td>Lowest Temperature</td> <td><math>-55\pm 5^{\circ}\text{C}</math></td> </tr> <tr> <td>Highest Temperature</td> <td><math>125\pm 5^{\circ}\text{C}</math></td> </tr> <tr> <td>Temperature-retaining time</td> <td>15 minutes each</td> </tr> </tbody> </table>  Refer to MIL-STD 202 Method 107	Testing Condition		Lowest Temperature	$-55\pm 5^{\circ}\text{C}$	Highest Temperature	$125\pm 5^{\circ}\text{C}$	Temperature-retaining time	15 minutes each	$\Delta R\%=\pm 1.0\%$
Testing Condition										
Lowest Temperature	$-55\pm 5^{\circ}\text{C}$									
Highest Temperature	$125\pm 5^{\circ}\text{C}$									
Temperature-retaining time	15 minutes each									
Loading Life in Moisture	Put the tested resistor in the chamber under temperature $40\pm 2^{\circ}\text{C}$ , relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.  Refer to JIS-C5201-1 4.24	1%: $\Delta R\%=\pm 1.5\%$ 5%: $\Delta R\%=\pm 3.0\%$								
Load Life	Put the tested resistor in chamber under temperature $70\pm 2^{\circ}\text{C}$ and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.  Refer to JIS-C5201-1 4.25	1%: $\Delta R\%=\pm 1.5\%$ 5%: $\Delta R\%=\pm 3.0$								



6.1 Technical application notes: (This is for recommendation, customer are please to perform adjustment according to actual application)

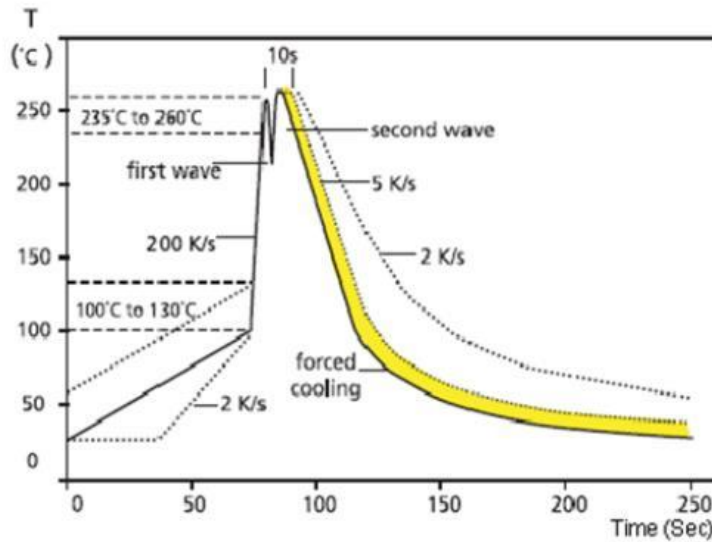
Soldering Profile :

6.1.1 Lead Free IR Reflow Soldering Profile(MEET J-STD-020D)



Remark: The peak temperature of soldering heat is  $260 \pm 5$  °C for 10 seconds.

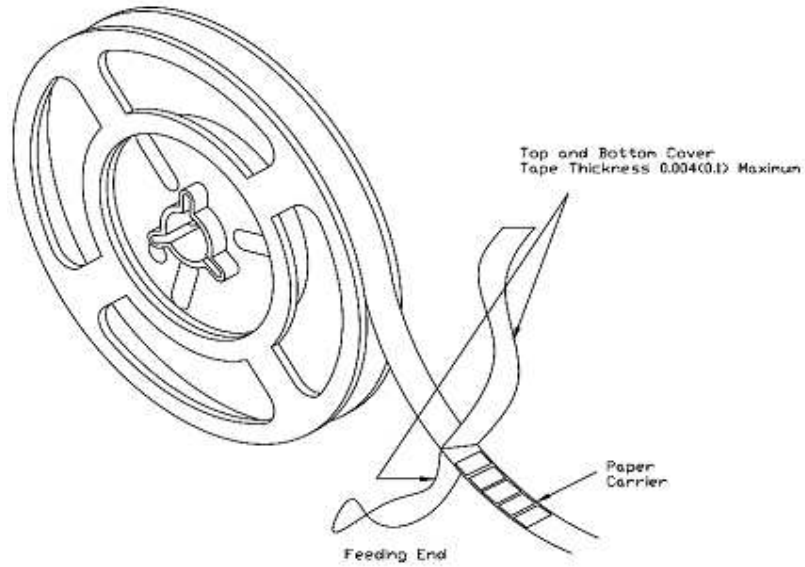
6.1.2 Lead Free Double Wave Soldering Profile.(This applies to 0603 size inclusive above product)



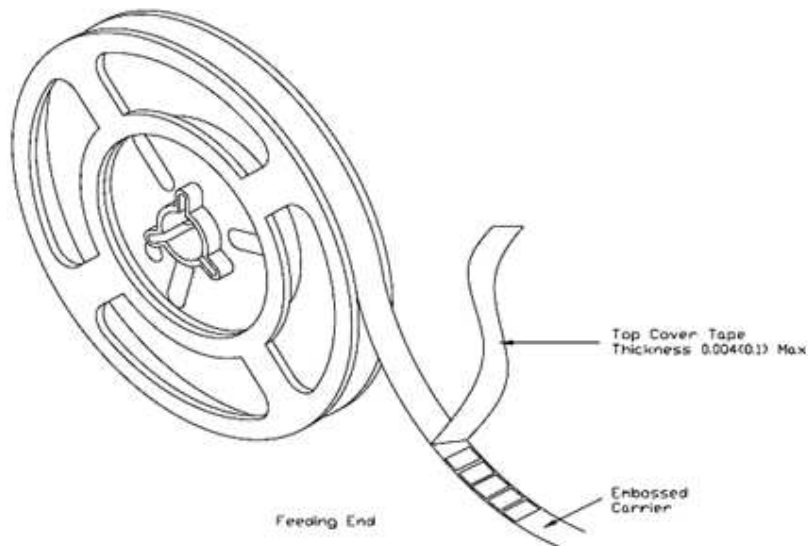
6.1.3 Soldering Iron: temperature  $350 \pm 10$  °C , dwell time shall be less than 3 sec.

7 TAPING

7.1 Structure of Taping  
Paper Carrier



Embossed Plastic Carrier



# FULLY LEAD-FREE HIGH POWER THICK FILM CHIP RESISTORS

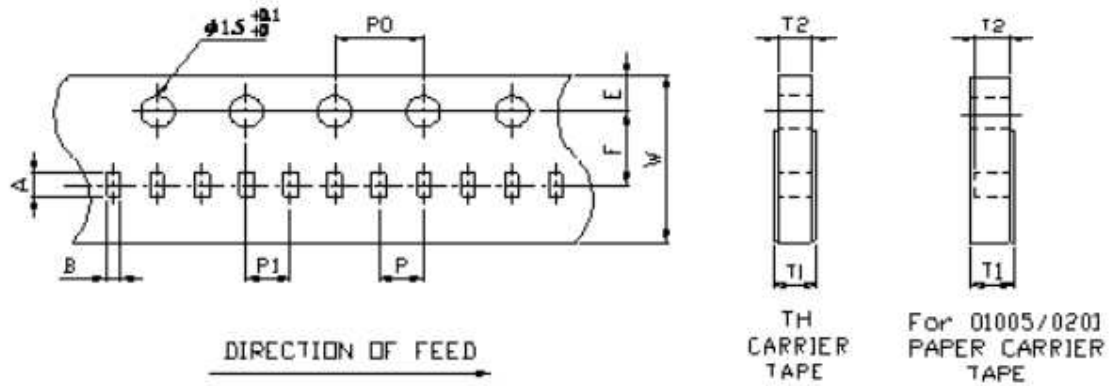
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## 7.2 Dimension

### 7.2.1 Dimension of Punched Paper Tape Carrier System (CPWF05,10)

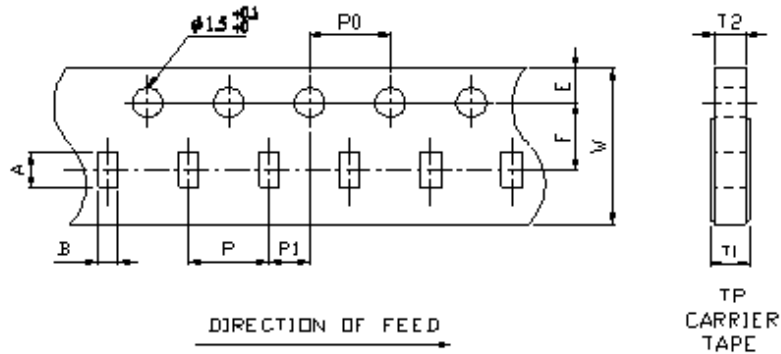


Remark : Pitch tolerance over any 10 pitches of Po is  $\pm 0.2$  mm

### Dimension of Punched Paper Tape Carrier System (CPWF05,10)

Type	Dimensions (mm)										
Size	A	B	W	E	F	T1	T2	P	P0	10xP0	P1
CPWF05	0.68 $\pm$ 0.05	0.38 $\pm$ 0.03	8.00 $\pm$ 0.10	1.75 $\pm$ 0.10	3.50 $\pm$ 0.05	0.42 $^{+0.1}_{-0}$	0.28 $\pm$ 0.02	2.00 $\pm$ 0.05	4.00 $\pm$ 0.05	40.0 $\pm$ 0.20	2.00 $\pm$ 0.05
CPWF10	1.15 $\pm$ 0.05	0.65 $\pm$ 0.05	8.00 $\pm$ 0.20	1.75 $\pm$ 0.10	3.50 $\pm$ 0.05	0.42 $^{+0.2}_{-0}$	0.40 $\pm$ 0.05	2.00 $\pm$ 0.10	4.00 $\pm$ 0.05	40.0 $\pm$ 0.20	2.00 $\pm$ 0.05

### 7.2.2 Dimension of Punched Paper Tape Carrier System /Plastic Embossed Carrier System (CPWF-16,21,32, 40, 50, 63)



Remark : Pitch tolerance over any 10 pitches of Po is  $\pm 0.2$  mm



# FULLY LEAD-FREE HIGH POWER THICK FILM CHIP RESISTORS

CPWF Series

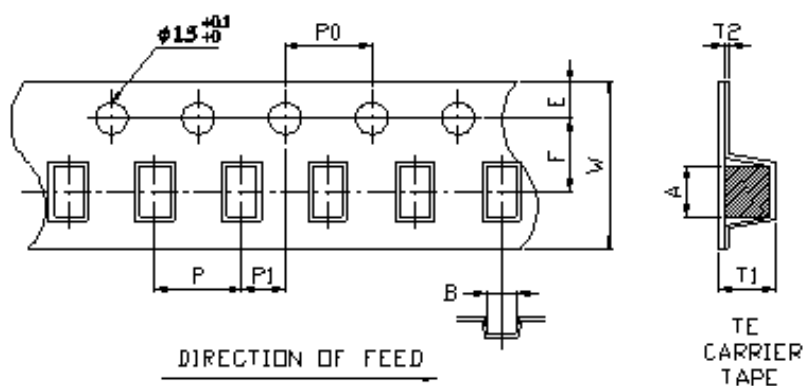
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Dimension of Punched Paper Tape Carrier System (CPWF – 16,21,32,40)

Type	Dimensions (mm)										
Size	A	B	W	E	F	T1	T2	P	P0	10xP0	P1
CPWF16	1.8±0.10	1.0±0.10	8.0±0.20	1.75±0.10	3.50±0.05	0.60 <sup>+0.2</sup> <sub>-0</sub>	0.60±0.10	4.0±0.10	4.0±0.05	40.00±0.20	2.0±0.05
CPWF21	2.3±0.10	1.55±0.1	8.0±0.20	1.75±0.10	3.50±0.05	0.75 <sup>+0.2</sup> <sub>-0</sub>	0.75±0.10	4.0±0.10	4.0±0.05	40.00±0.20	2.0±0.05
CPWF32	3.50±0.20	1.90±0.20	8.00±0.20	1.75±0.10	3.50±0.05	0.75 <sup>+0.2</sup> <sub>-0</sub>	0.75±0.10	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05
CPWF40	3.50±0.20	2.80±0.20	8.00±0.20	1.75±0.10	3.50±0.05	0.75 <sup>+0.2</sup> <sub>-0</sub>	0.75±0.10	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05

Dimension of Plastic Embossed Carrier System (CPWF -50, 63)



Type	Dimensions (mm)										
Size	A	B	W	E	F	T1	T2	P	P0	10xP0	P1
CPWF50	5.50±0.20	2.80±0.20	12.00±0.20	1.75±0.10	5.50±0.05	1.10±0.15	0.23±0.15	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05
CPWF63	6.70±0.20	3.40±0.20	12.00±0.20	1.75±0.10	5.50±0.05	1.10±0.15	0.23±0.15	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05



Product Specification

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7.3 Packaging

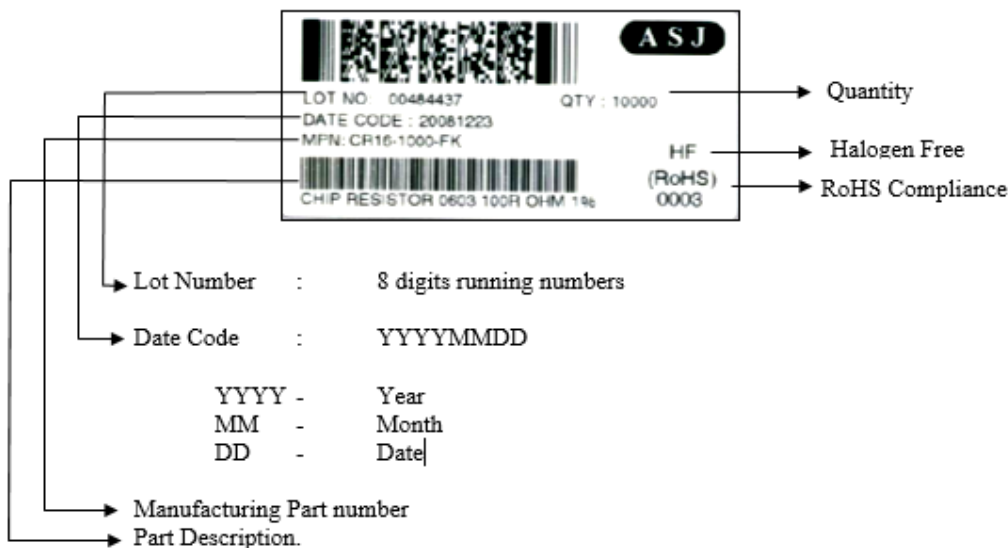
7.3.1 Taping

7.3.1.1 Quantity – Tape and Reels

Code	Quantity	Reel	Remark
CPWF05 CPWF10	10,000 pcs	7"	2mm pitch
	20,000 pcs	7"	2mm pitch
	50,000 pcs	13"	2mm pitch
CPWF16	5,000 pcs	7"	4mm pitch
CPWF21	10,000 pcs	10"	4mm pitch
CPWF32 CPWF40	20,000 pcs	13"	4mm pitch
CPWF50	4,000 pcs	7"	Embossed 4mm pitch
CPWF63	4,000 pcs	7"	Embossed 4mm pitch

7.3.2 Identification

Production label that indicates the 8 digits lot number, product type, resistance value and tolerance shall be pasted on the surface of each reel.

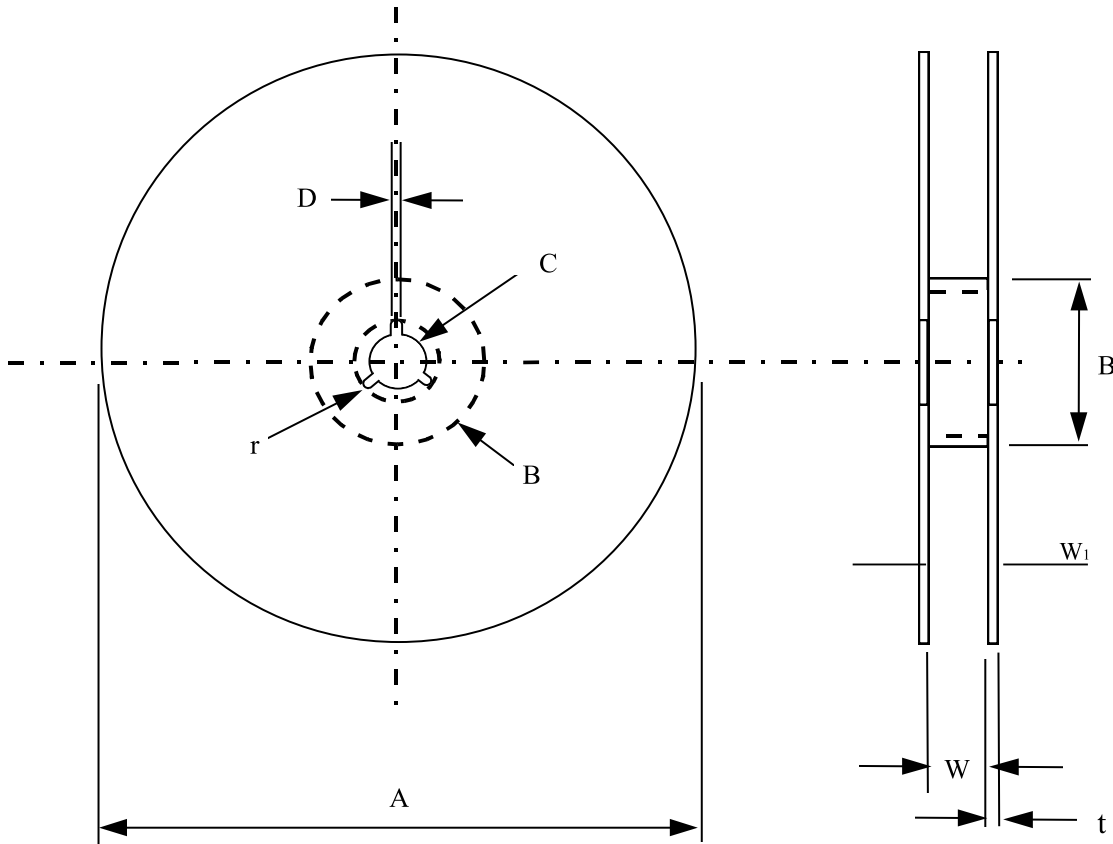


7.3.3 Packaging Reel Box

Dimension	Reel Box	Number of Reels
185 × 60 × 186 mm	25K Box	5
185 × 120 × 186 mm	50K Box	10



7.3.4 Reel Dimensions

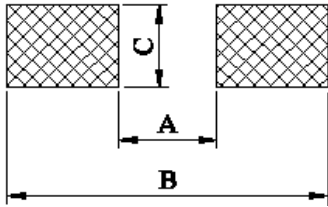


Model	A	B	C	D	W	W <sub>1</sub>	t	r
7" Reel (5K) (except 0402 10K)	φ178±2.0	φ80min	13± 0.2	φ2.0± 0.5	11± 0.1	14.4 max	1.0± 0.1	1.0
7" Reel (4K)	φ178±2.0	φ60min	13± 0.2	φ2.0± 0.5	13±1.0	14.4 max	1.2± 0.1	1.0
7" Reel (2K) (for 2512)	φ178±2.0	φ60min	13.5±0.5	φ2.0± 0.5	13.8±0.5	14.4 max	1.2± 0.1	1.0
10" Reel (10K)	φ254±2.0	φ60min	13± 0.2	φ2.0± 0.5	11± 1.0	14.4 max	1.5± 0.1	1.0
13" Reel (20K)	φ330±2.0	φ60min	13± 0.2	φ2.0± 0.5	11± 1.0	14.4 max	2.1± 0.1	-
13" Reel (20K)	φ330±1.0	φ100±1	13.5± 0.5	2~3± 0.5	10± 0.5	-	-	-

## 8 SURFACE MOUNT LAND PATTERNS Design (For Reflow Soldering)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

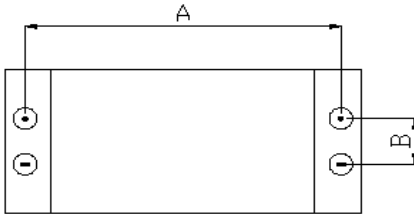
Unit:mm



TYPE	DIM	A	B	C
CPWF05		0.3	1.0	0.4
CPWF10		0.5	1.5	0.6
CPWF16		0.8	2.1	0.9
CPWF21		1.2	3.0	1.3
CPWF32		2.2	4.2	1.6
CPWF40		2.2	4.2	2.8
CPWF50		3.5	6.1	2.8
CPWF63		3.8	8.0	3.5

## 9 Measurement Point

Bottom electrode		Unit : mm	
TYPE	DIM	A	B
		CPWF05	0.44±0.05
CPWF10		0.80±0.05	0.24±0.05
CPWF16		1.35±0.05	0.35±0.05
CPWF21		1.80±0.05	0.35±0.05
CPWF32		2.90±0.05	0.35±0.05
CPWF40		2.90±0.05	0.35±0.05
CPWF50		4.50±0.05	1.15±0.05
CPWF63		5.90±0.05	1.60±0.05



⊙ Current Terminal  
 ⊖ Voltage Terminal

# FULLY LEAD-FREE HIGH POWER THICK FILM CHIP RESISTORS

CPWF Series

DS-ENG-072

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## 10 REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version.1	05.11.2019		Initial Release
Version.2	31.12.2019		Typo error in clause 4
Version.3	10.02.2021		Add in product CPWF05 – CPWF21 Revise clause 2 Part Numbering System Revise clause 3.1.1 Resistor rated power Revise clause 3.2 Power Derating curve Revise clause 3.5 Storage temperature range Revise clause 3.10 TCR table Revise clause 4 Marking on product Revise clause 5.1 Dimension Revise clause 6.0.1 item Dielectric withstand voltage Revise clause 6.0.2 item Joint strength of solder Revise clause 7.2.1 & 7.2.2 dimension of punch paper Revise clause 7.3.1.1 tape and reel qty Revise clause 8 land pattern Revise clause 9 measurement point
Version.4	07.03.2023		Revise clause 2 Part Numbering System Revise clause 3.2 Power derating curve Revise clause 3.8 Product Assurance Revise clause 3.10 TCR table Revise clause 4 Marking on product table Revise clause 6.0.1 item intermittent overload Revise clause 6.0.2 item Terminal strength Revise clause 6.1.1 IR reflow soldering profile



# DATA SHEET

## High Power Pulse-Proof Thick Film Chip Resistor

### CPWS Series

1% TO 5%, TCR  $\pm 200$

SIZE: 0402/0603/0805/1206/2010/2512



# HIGH POWER PULSE-PROOF THICK FILM CHIP RESISTOR

CPWS Series

DS-ENG-079

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## 1. SCOPE

- 1.1 This specification is applicable to Lead-free and Halogen-free of RoHS directive for CPWS series Pulse-Proof thick film chip resistors.
- 1.2 The product is for general electronic purpose.

## 2. PART NUMBERING SYSTEM

Part Numbering is made in accordance with the following system:

Ordering Code / Information

CPWS	63	-	XXXX		-	F	E
Type	Size (Inch / mm)	Nominal Resistance			Resistance Tolerance	Packaging	
High Power Pulse-Proof Thick Film Chip Resistors	10(0402)	Resistors	5% (3-Digit)	EX. 10Ω=100 4.7Ω=4R7	F=±1% J=±5%	E = 4,000 pcs Lead Free L = 5,000 pcs Lead Free K = 10,000 pcs Lead Free Y = 20,000 pcs Lead Free N = 50,000 pcs Lead Free	
	16(0603)		1% (4-Digit)	EX. 10.2Ω=10R2 10KΩ=1002			
	21(0805)						
	32(1206)						
	50(2010)						
	63(2512)						

## 3. RATING

### 3.1 Rated Power

#### 3.1.1 Resistor Rated Power

Type	Rate Power at 70°C	Max. Working Current	Max. Overload Current
CPWS10 (0402)	$\frac{1}{5}$ W	50V	100V
CPWS16 (0603)	$\frac{1}{3}$ W	75V	150V
CPWS21 (0805)	$\frac{1}{2}$ W	150V	300V
CPWS32 (1206)	$\frac{3}{4}$ W	200V	400V
CPWS50 (2010)	1W	200V	400V
CPWS63 (2512)	2W	200V	400V



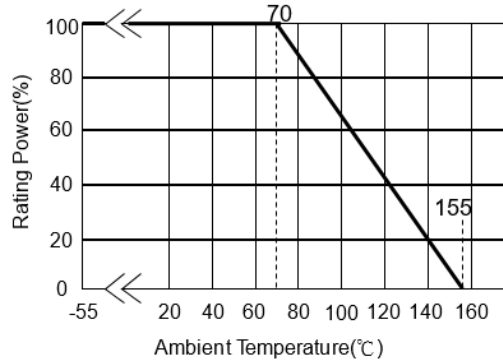
Product Specification

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### 3.2 Power Derating Curve:

Operating Temperature Range:- 55 ~ 155 °C

If the ambient temperature exceeds 70 degrees centigrade to 155 degrees centigrade, the power can be modified by the curve as below.



### 3.3 Standard Atmospheric Condition

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient Temperature = + 5°C to +35°C

Relative Humidity = < 85% RH

Air Pressure = 86 kPa to 106kPa

If there may be any doubt about the results, measurement shall be made within the following limits:

Ambient Temperature = 20 ± 2°C

Relative Humidity = 60 to 70% RH

Air Pressure = 86 kPa to 106kPa

3.4 Operating Temperature Range -55°C to +155°C

3.5 Storage Temperature Range -5°C to + 40°C / < 85% RH

3.6 Flammability Rating Tested in accordance to UL-94, V-0

3.7 Moisture Sensitivity Level Rating: Level 1

3.8 Product Assurance

ASJ resistor shall warranty 24 months from manufacturing date with control conditions.

3.9 ASJ resistors are RoHS-compliant in accordance to RoHS Directive.



# HIGH POWER PULSE-PROOF THICK FILM CHIP RESISTOR

CPWS Series

DS-ENG-079

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## 3.10 Resistance, Resistance Tolerance and Temperature Coefficient of Resistance.

Type	Rate Power at 70°C	Max. Working Current	Max. Overload Current	T.C.R (ppm/°C)	Resistance Range
					F(±1%) · J(±5%) E-24 · E-96
CPWS10 (0402)	$\frac{1}{5}$ W	50V	100V	±200	$1\Omega \leq R \leq 10M$
CPWS16 (0603)	$\frac{1}{3}$ W	75V	150V	±200	$1\Omega \leq R \leq 10M$
CPWS21 (0805)	$\frac{1}{2}$ W	150V	300V	±200	$1\Omega \leq R \leq 10M$
CPWS32 (1206)	$\frac{3}{4}$ W	200V	400V	±200	$1\Omega \leq R \leq 10M$
CPWS50 (2010)	1W	200V	400V	±200	$1\Omega \leq R \leq 10M$
CPWS63 (2512)	2W	200V	400V	±200	$1\Omega \leq R \leq 10M$
<b>Operating Temperature Range</b>					-55°C ~ +155°C

### 3.11 Rated Voltage:

Rated Voltage: DC voltage or AC voltage (rms.) based on the rated power.

The voltage can be calculated by the following formula. If the calculated value exceeds the Max. voltage specified in the Table 3.10, the Max. voltage rating is set as the voltage rating.

$$E = \sqrt{R \times P}$$

E= Rated voltage (v)

P= Power rating (w)

R= Nominal resistance(Ω)



Product Specification

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# HIGH POWER PULSE-PROOF THICK FILM CHIP RESISTOR

CPWS Series

DS-ENG-079

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## 4. MARKING ON PRODUCT

The nominal resistance shall be marked on the surface of each resistor

Part Number	Color	Marking on Product
CPWS10(0402)	---	No Marking
CPWS16(0603) CPWS21(0805) CPWS32(1206) CPWS50(2010) CPWS63(2512)	Light Yellow	1) Tolerance; $\pm 1\%$ (F) Four Numerals Marking - 0603 three characters marking base on E-96 marking standard 2) Tolerance; $\pm 5\%$ (F) Three Numerals Marking

### 4.1 Numeric Numbering

#### 4.1.1 1% Tolerance : **Four Numerals Marking**

First 3 digits are significant figures, fourth digit is number of zeros.

Examples:

Nominal Resistance	Marking	Remarks
1 $\Omega$	1R00	$1 \times 10^0 = 1$
10 $\Omega$	10R0	$10 \times 10^0 = 10$
100 $\Omega$	1000	$100 \times 10^0 = 100$
4.7K $\Omega$	4701	$470 \times 10^1 = 4700$
47K $\Omega$	4702	$470 \times 10^2 = 47000$
470K $\Omega$	4703	$470 \times 10^3 = 470000$
1M $\Omega$	1004	$100 \times 10^4 = 1000000$

#### 4.1.2 0603 1% tolerance: **Three Character E-96 Marking Standard.**

The first 2 digits for the 3 digits E-96 part marking standard, (Refer Table 2 & 3).

The third character is a letter multiplier:

Nominal resistance	Marking	Remark
33.2 $\Omega$	51 X	$332 \times 10^{-1} \Omega$
150 $\Omega$	18 A	$150 \times 10^0 \Omega$
4.99K $\Omega$	68 B	$499 \times 10^1 \Omega$
1 0.2K $\Omega$	02 C	$102 \times 10^2 \Omega$
100K $\Omega$	01 D	$100 \times 10^3 \Omega$



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## 4.1.3 EIA-96 Marking Scheme

**Table 2 Significant figures**

Significant Figures	Symbol	Significant Figures	Symbol	Significant Figures	Symbol	Significant Figures	Symbol
100	01	178	25	316	49	562	73
102	02	182	26	324	50	576	74
105	03	187	27	332	51	590	75
107	04	191	28	340	52	604	76
110	05	196	29	348	53	619	77
113	06	200	30	357	54	634	78
115	07	205	31	365	55	649	79
118	08	210	32	374	56	665	80
121	09	215	33	383	57	681	81
124	10	221	34	392	58	698	82
127	11	226	35	402	59	715	83
130	12	232	36	412	60	732	84
133	13	237	37	422	61	750	85
137	14	243	38	432	62	768	86
140	15	249	39	442	63	787	87
143	16	255	40	453	64	806	88
147	17	261	41	464	65	825	89
150	18	267	42	475	66	845	90
154	19	274	43	487	67	866	91
158	20	280	44	499	68	887	92
162	21	287	45	511	69	909	93
165	22	294	46	523	70	931	94
169	23	301	47	536	71	953	95
174	24	309	48	549	72	976	96

**Table 3 Multiplier**

Symbol	Multiplier	Symbol	Multiplier
A	10 <sup>0</sup>	G	10 <sup>6</sup>
B	10 <sup>1</sup>	H	10 <sup>7</sup>
C	10 <sup>2</sup>	X	10 <sup>-1</sup>
D	10 <sup>3</sup>	Y	10 <sup>-2</sup>
E	10 <sup>4</sup>		
F	10 <sup>5</sup>		

# HIGH POWER PULSE-PROOF THICK FILM CHIP RESISTOR

CPWS Series

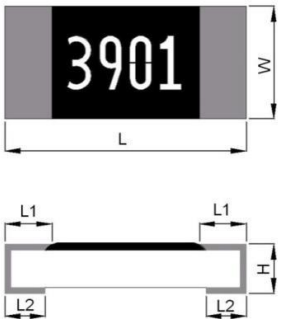
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## 5. DIMENSION, CONSTRUCTION AND MATERIAL

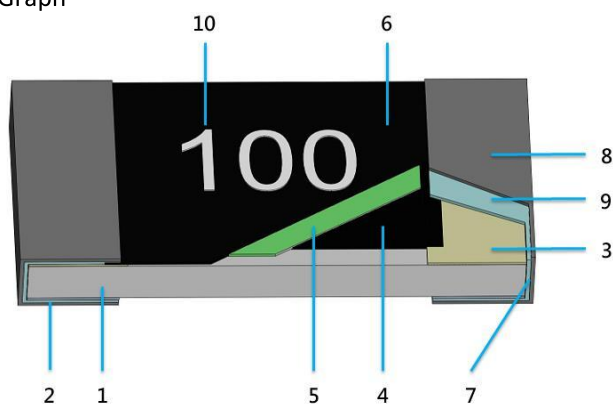
### 5.1 Dimension

Unit:mm



Type	Dimension Size Code	L	W	H	L1	L2
CPWS10	0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
CPWS16	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
CPWS21	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
CPWS32	1206	3.05±0.10	1.55±0.10	0.50 <sup>+0.10</sup> <sub>-0.05</sub>	0.45±0.20	0.35±0.15
CPWS50	2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
CPWS63	2512	6.40±0.20	3.20±0.20	0.70±0.10	0.60±0.20	1.25±0.20

### 5.2 Structure Graph



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Terminal inner electrode
3	Top inner electrode	8	Ni plating
4	Resistive layer	9	Sn plating
5	1st Protective coating	10	MK layer

### 5.3 Plating Thickness

5.3.1 Ni:  $\geq 2 \mu\text{m}$

5.3.2 Sn (Tin):  $\geq 3 \mu\text{m}$

5.3.3 Sn (Tin): Matte Sn



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# HIGH POWER PULSE-PROOF THICK FILM CHIP RESISTOR

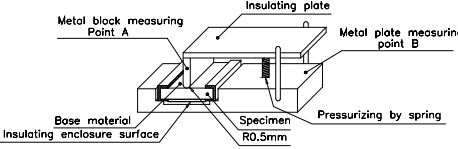
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## 6. RELIABILITY TEST

### 6.1 Electrical Performance Test

Item	Conditions	Specifications
		Resistors
Temperature Coefficient of Resistance	<p><b>Refer to JIS-C5201-1 4.8</b></p> $TCR(ppm/^{\circ}C) = \frac{(R2 - R1)}{R1(T2 - T1)} \times 10^6$ <p>R1: Resistance at room temperature R2: Resistance at -55°C or +125°C T1: Room temperature T2: Temperature -55°C or +125°C</p>	Refer to item 3.10 general specifications
Short Time Overload	<p><b>Refer to JIS-C5201-1 4.13</b></p> <p>Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3.10)</p>	<p>±1%: ΔR=±1.0%</p> <p>±5%: ΔR=±2.0%</p>
Insulation Resistance	<p><b>Refer to JIS-C5201-1 4.6</b></p> <p>Put the resistor in the fixture, add 100 VDC in +, - terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material.</p> 	≥10 <sup>9</sup> Ω
Dielectric Withstand Voltage	<p><b>Refer to JIS-C5201-1 4.7</b></p> <p>Put the resistor in the fixture, add VAC (see spec. below) in +, - terminal for. CPWS10, 16 apply 300 VAC 1 minute CPWS21, 32, 50, 63 apply 500 VAC 1 minute.</p>	No short or burned on the appearance.
ESD	<p><b>Refer to EIAJED-4701-300 304</b></p> <p>Put the specimens on the test fixture and apply ±3KVDC on terminals for 1sec. Afterwards, the specimens stabilize for 30min or more and measure of its resistance variance rate.</p>	ΔR%=±5.0%



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## 6.2 Mechanical Performance Test

Item	Conditions	Specifications
		Resistors
Terminal Strength	<p><b>Refer to JIS-C5201-1 4.16</b></p> <p>Test1: The resistor mounted on the board applied 5N pushing force on the sample rear for 10 sec.</p> <p>Test2: The resistor mounted on the board slowly add force on the sample rear until the sample termination is breakdown.</p>	<p>Test1: No evidence of mechanical damage.</p> <p>Test2: <math>\geq 5N</math></p>
Resistance to Solvent	<p><b>Refer to JIS-C5201-1 4.29</b></p> <p>The tested resistor be immersed into isopropyl alcohol of 20~25°C for 5 minutes, then the resistor is left in the room for 48 hrs., and measured its resistance variance rate.</p>	<p><math>\Delta R\% = \pm 1.0\%</math></p>
Solderability	<p><b>Refer to JIS-C5201-1 4.17</b></p> <p>Preconditioning: Put the tested resistor in the apparatus of PCT, at a temperature of 105°C, humidity of 100% RH, and pressure of <math>1.22 \times 10^5</math> Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more.</p> <p>Test method: The resistor be immersed into solder pot in temperature <math>235 \pm 5^\circ C</math> for <math>2 \pm 0.5</math> sec, then the resistor is left as placed under microscope to observe its solder area.</p>	<p>Solder coverage over 95%</p>
Resistance to Soldering Heat	<p><b>Refer to JIS-C5201-1 4.18</b></p> <p>©Test method 1 (solder pot test): The tested resistor be immersed into molten solder of <math>260^{+5}_{-0}^\circ C</math> for <math>10^{+1}_{-0}</math> seconds. Then the resistor is left in the room for 1 hour.</p> <p>©Test method 2 (solder pot test): The tested resistor be immersed into molten solder of <math>260^{+5}_{-0}^\circ C</math> for <math>30^{+1}_{-0}</math> seconds. Then the resistor is left as placed under microscope to observe its solder area.</p> <p>©Test method 3 (Electric iron test): Preheating temperature: <math>350 \pm 10^\circ C</math> Electric iron preheating time : <math>3^{+1}_{-0}</math> sec Preheating the electric iron on electrode termination, as after that step placed the iron over 60 min. and measured its resistance variance rate.</p>	<p>Test item 1: (1) Variance rate on resistance <math>\Delta R\% = \pm 1.0\%</math></p> <p>Test item 2: (1) Solder coverage over 95%. (2) The underlying material (such as ceramic) shall not be visible at the crest corner area of the electrode.</p> <p>Test item 3: (1) Variance rate on resistance <math>\Delta R\% = \pm 1.0\%</math></p>



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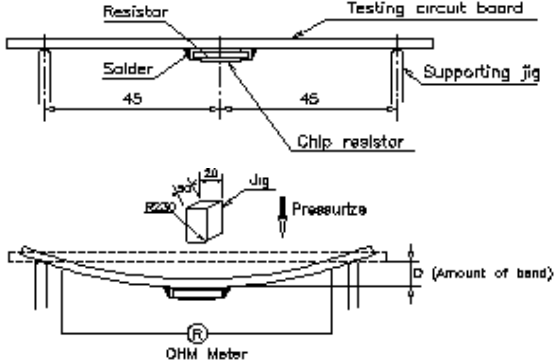
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Item	Conditions	Specifications
Joint Strength of Solder	<p><b>Refer to JIS-C5201-1 4.33</b></p> <p>◎Bending Strength: Solder tested resistor on to PC board. Add force in the middle down, and under load measured its resistance variance rate.</p> <p>D:CPWS10, 16, 21=5mm CPWS32=3mm CPWS50, 63=2mm</p> 	$\Delta R\% = \pm 1.0\%$



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## 6.3 Environmental Test

Item	Conditions	Specifications								
		Resistors								
Resistance to Dry Heat	<p><b>Refer to JIS-C5201-1 4.25</b> Put tested resistor in chamber under temperature <math>155\pm 5^{\circ}\text{C}</math> for <math>1000_{-0}^{+48}</math> hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.</p>	$\pm 1\%$ : $\Delta R = \pm 1.0\%$ $\pm 5\%$ : $\Delta R = \pm 2.0\%$								
Thermal Shock	<p><b>Refer to MIL-STD 202 Method 107</b> Put the tested resistor in the chamber under the Thermal Shock which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and measure its resistance variance rate.</p> <table border="1" style="margin-left: 40px;"> <thead> <tr> <th colspan="2">Testing Condition</th> </tr> </thead> <tbody> <tr> <td>Lowest Temperature</td> <td><math>-55\pm 5^{\circ}\text{C}</math></td> </tr> <tr> <td>Highest Temperature</td> <td><math>125\pm 5^{\circ}\text{C}</math></td> </tr> <tr> <td>Temperature-retaining time</td> <td>15 minutes each</td> </tr> </tbody> </table>	Testing Condition		Lowest Temperature	$-55\pm 5^{\circ}\text{C}$	Highest Temperature	$125\pm 5^{\circ}\text{C}$	Temperature-retaining time	15 minutes each	$\Delta R = \pm 1.0\%$
Testing Condition										
Lowest Temperature	$-55\pm 5^{\circ}\text{C}$									
Highest Temperature	$125\pm 5^{\circ}\text{C}$									
Temperature-retaining time	15 minutes each									
Loading Life in Moisture	<p><b>Refer to JIS-C5201-1 4.24</b> Put the tested resistor in the chamber under temperature <math>40\pm 2^{\circ}\text{C}</math>, relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.</p>	$\pm 1\%$ : $\Delta R = \pm 1.0\%$ $\pm 5\%$ : $\Delta R = \pm 2.0\%$								
Load Life	<p><b>Refer to JIS-C5201-1 4.25</b> Put the tested resistor in chamber under temperature <math>70\pm 2^{\circ}\text{C}</math> and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate.</p>	$\pm 1\%$ : $\Delta R = \pm 1.0\%$ $\pm 5\%$ : $\Delta R = \pm 2.0\%$								



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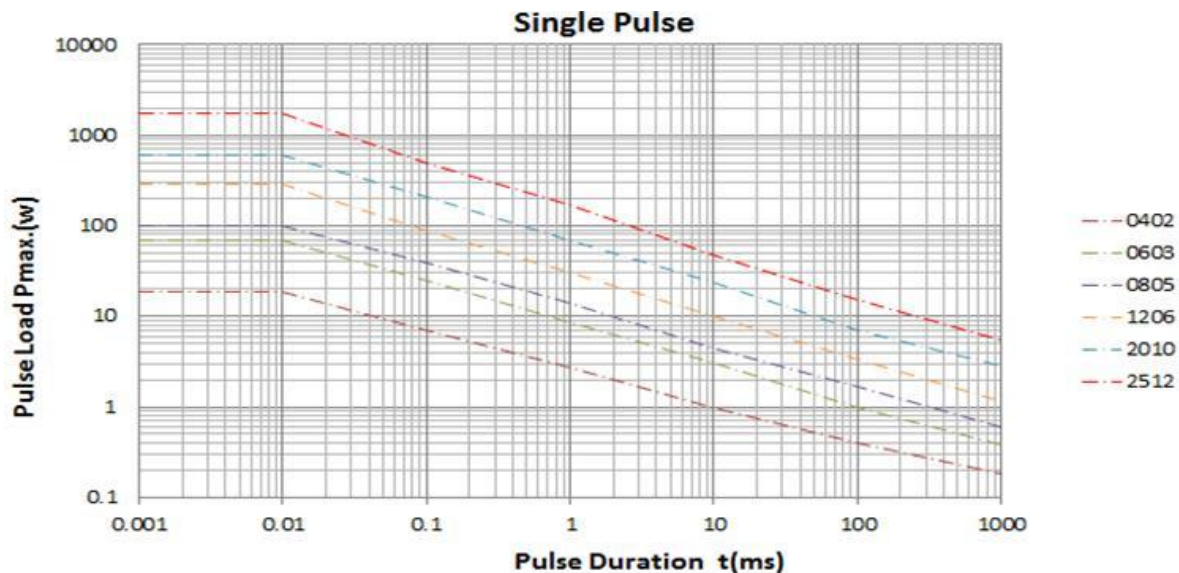
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## 6.4 Pulse Loading Capability

### 6.4.1 Single Pulse Load:

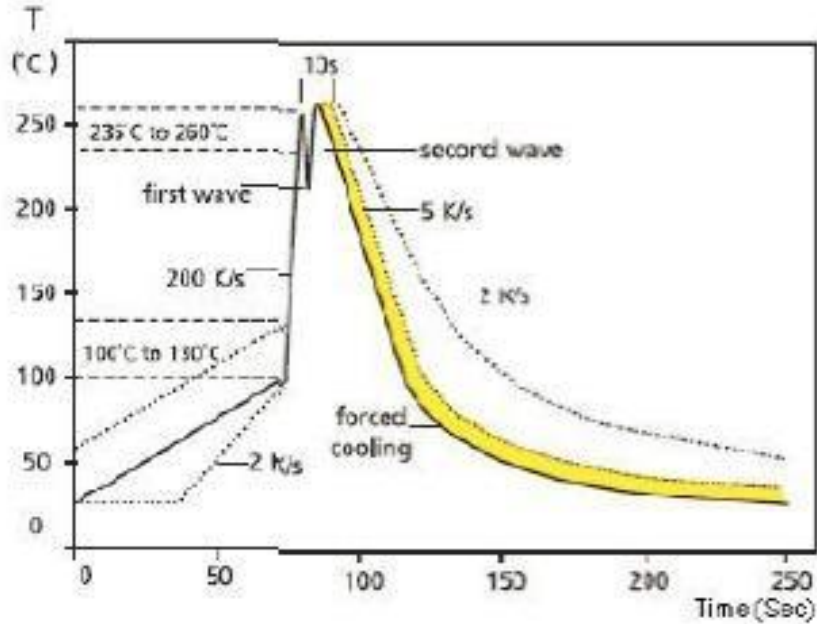
Pulse on a regular basis; maximum permissible peak pulse power ( $P_{max}$ ) as a function of a pulse duration.  $V_{peak} \leq 0402(100V)$ 、 $0603(150V)$ 、 $0805(300V)$ 、 $1206(400V)$ 、 $2010(400V)$ 、 $2512(400V)$



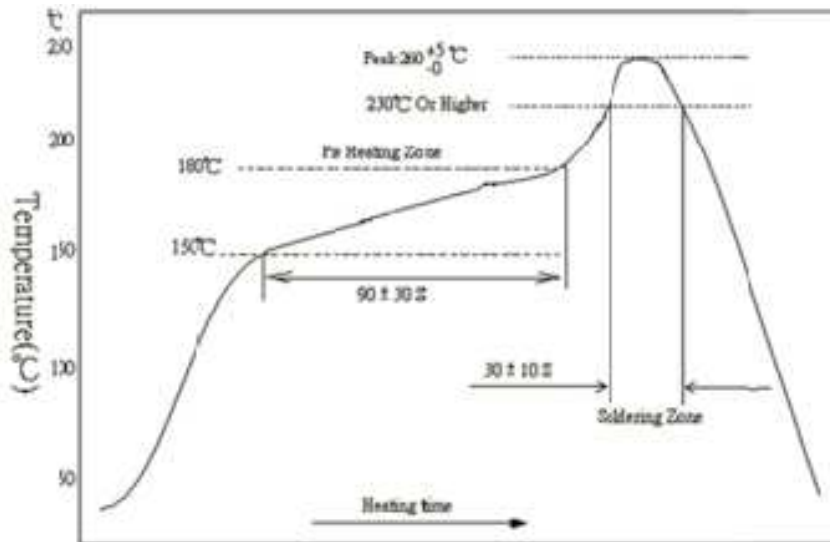
6.5 Technical application notes: (This is for recommendation, customer please perform adjustment according to the actual application)

Soldering Profile

6.5.1 Lead free double wave soldering profile (This applies to 0603 size inclusive above products)



6.5.2 Lead Free IR Reflow Soldering Profile



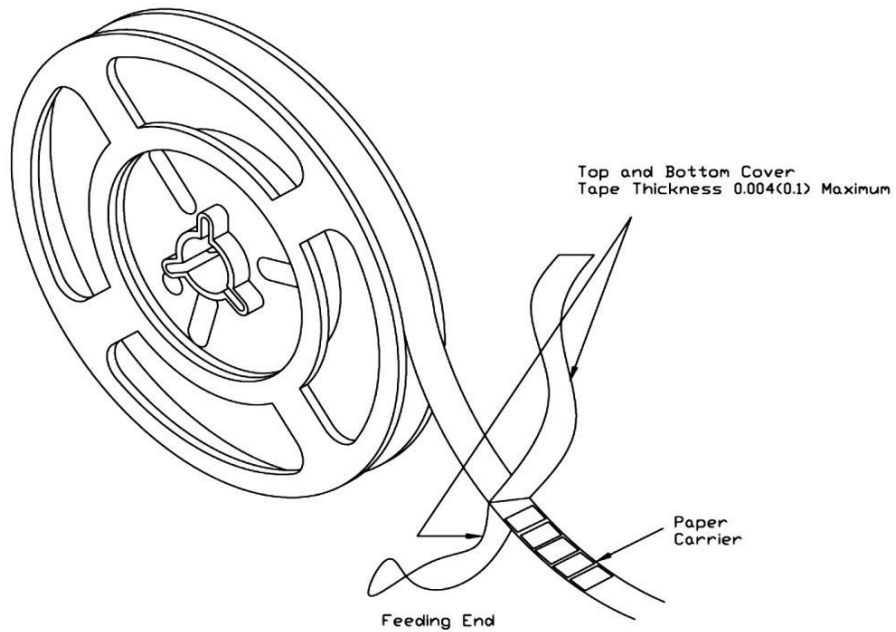
Remark: The peak temperature of soldering heat is 260 +5/-0 °C for 10 seconds

6.5.3 Soldering Iron: Temperature 350°C ± 10°C, dwell time shall be less than 3 sec.

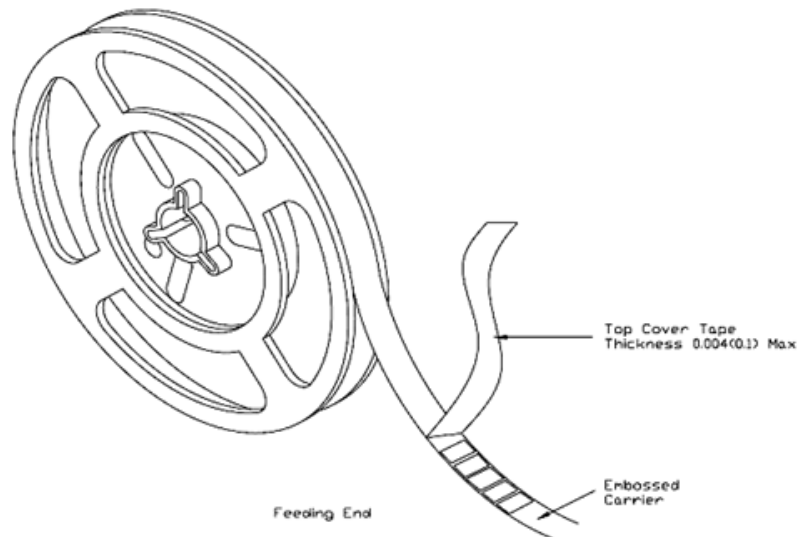
## 7 TAPING

### 7.1 Structure of Taping

#### Paper Carrier



#### Embossed Plastic Carrier



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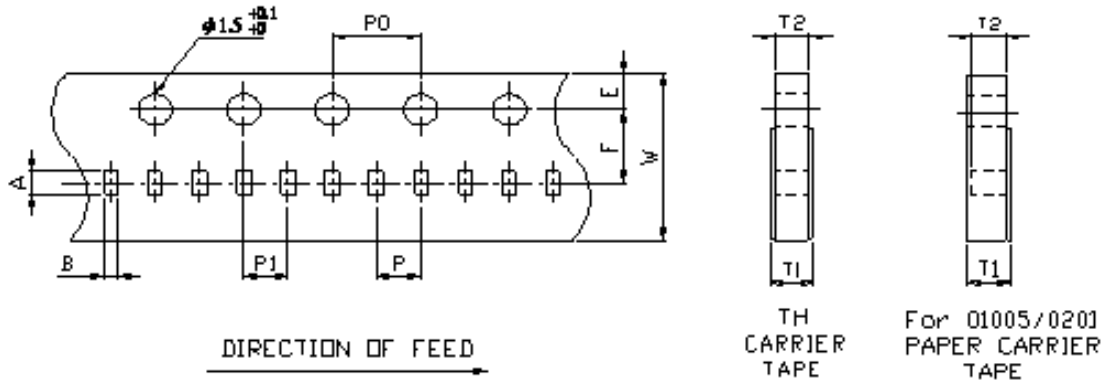
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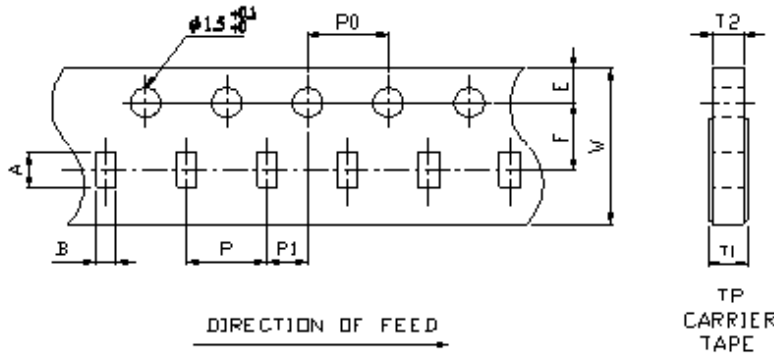
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## 7.2 Dimension

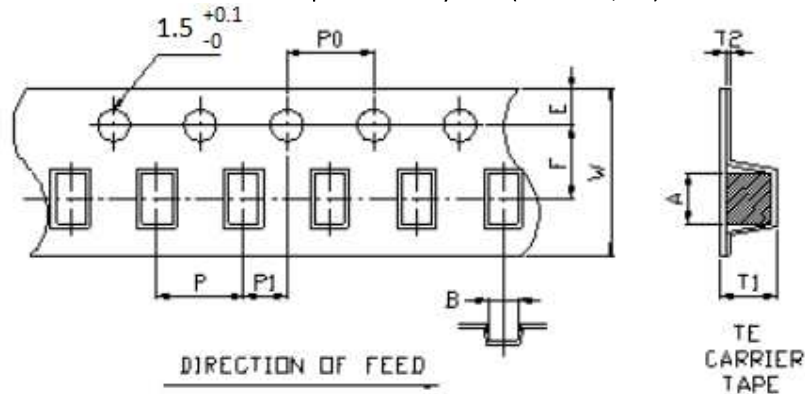
### 7.2.1 Dimension of Punched Paper Tape Carrier System(CPWS10)



### 7.2.2 Dimension of Punched Paper Tape Carrier System(CPWS16, 21, 32)



### 7.2.3 Dimension of Embossed Tape Carrier System(CPWS50, 63)



### Dimension of Punched Paper Tape Carrier System (CPWS – 63)

Code	A	B	W	E	F	T1	T2	P	P0	P1
CPWS10	1.15±0.05	0.65±0.05	8.00±0.20	1.75±0.10	3.50±0.05	0.40± <sub>0</sub> <sup>+0.2</sup>	0.40±0.05	2.00±0.10	4.00±0.05	2.00±0.05
CPWS16	1.80±0.10	1.00±0.10	8.00±0.20	1.75±0.10	3.50±0.05	0.60± <sub>0</sub> <sup>+0.2</sup>	0.60±0.10	2.00±0.10	4.00±0.05	2.00±0.05
CPWS21	2.30±0.10	1.55±0.10	8.00±0.20	1.75±0.10	3.50±0.05	0.75± <sub>0</sub> <sup>+0.2</sup>	0.75±0.10	4.00±0.10	4.00±0.05	2.00±0.05
CPWS32	3.50±0.20	1.90±0.20	8.00±0.20	1.75±0.10	3.50±0.05	0.75± <sub>0</sub> <sup>+0.2</sup>	0.75±0.10	4.00±0.10	4.00±0.05	2.00±0.05
CPWS50	5.50±0.20	2.80±0.20	12.00±0.20	1.75±0.10	5.50±0.05	1.10±0.15	0.23±0.15	4.00±0.10	4.00±0.05	2.00±0.05
CPWS63	6.70±0.20	3.40±0.20	12.00±0.20	1.75±0.10	5.50±0.05	1.10±0.15	0.23±0.15	4.0±0.10	4.00±0.05	2.00±0.05



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## 7.3 Packaging

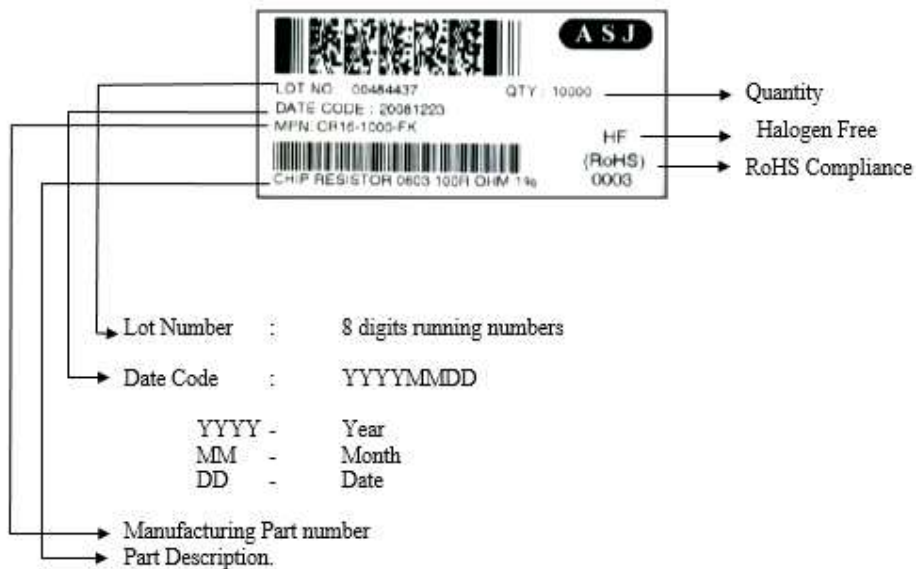
### 7.3.1 Taping

### 7.3.2 Quantity – Tape and Reels

Type	Qty(pcs/reel)	Reel	Remark
CPWS10	10,000	7"	Paper 2mm pitch
	20,000	7"	Paper 2mm pitch
	50,000	13"	Paper 2mm pitch
CPWS16	5,000	7"	Paper 4mm pitch
CPWS21	10,000	10"	Paper 4mm pitch
CPWS32	20,000	13"	Paper 4mm pitch
CPWS50 CPWS63	4,000	7"	Embossed 4mm pitch

## 7.4 Identification

Production label that indicates the 8 digits lot number, product type, resistance value and tolerance shall be pasted on the surface of each reel.



### 7.4.1 Packaging Reel Box

Dimension	Reel Box	Number of Reels
185 × 60 × 186 mm	25K Box	5
185 × 120 × 186 mm	50K Box	10



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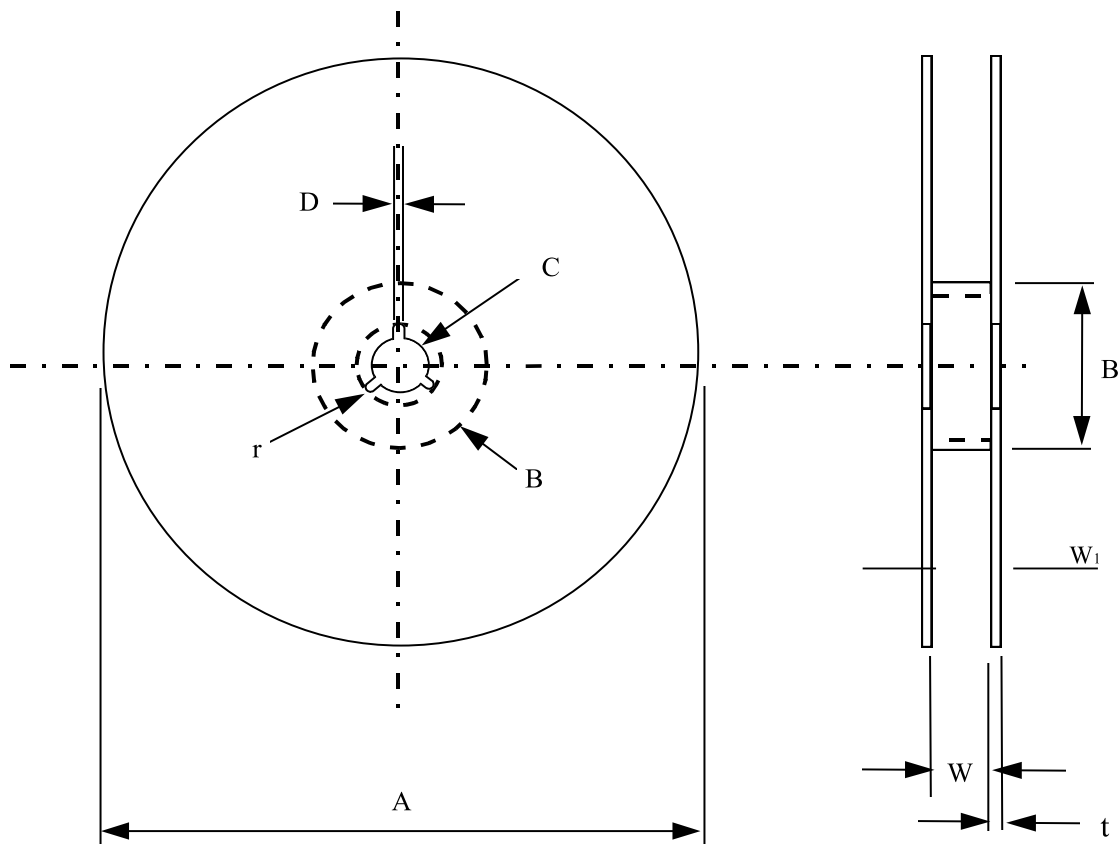
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## 7.4.2 Reel Dimensions



Model	A	B	C	D	W	W <sub>1</sub>	t	r
7" Reel (5K) (except 0402 10K)	φ178±2.0	φ60min	13±0.2	φ2.0±0.5	11±0.1	14.4 max	1.0±0.1	1.0
7" Reel (4K)	φ178±2.0	φ60min	13±0.2	φ2.0±0.5	13±1.0	14.4 max	1.2±0.1	1.0
7" Reel (10K)	φ178±2.0	φ60min	13±0.2	φ2.0±0.5	11±0.1	14.4 max	1.0±0.1	1.0
10" Reel (10K)	φ254±2.0	φ60min	13±0.2	φ2.0±0.5	11±1.0	14.4 max	1.5±0.1	1.0
13" Reel (20K, 50K)	φ330±2.0	φ60min	13±0.2	φ2.0±0.5	11±1.0	14.4 max	2.1±0.1	-
13" Reel (20K)	φ330±1.0	φ100±1	13.5±0.5	2~3±0.5	10±0.5	-	-	-



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# HIGH POWER PULSE-PROOF THICK FILM CHIP RESISTOR

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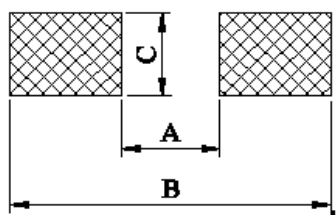
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## 8 SURFACE MOUNT LAND PATTERNS DESIGN (FOR REFLOW SOLDERING)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

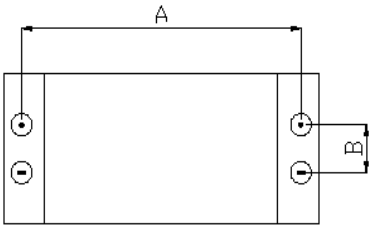
Unit:mm



TYPE \ DIM	A	B	C
CPWS10	0.50	1.80	0.50
CPWS16	0.50	2.50	1.27
CPWS21	0.66	3.56	1.78
CPWS32	1.50	4.30	2.18
CPWS50	3.50	6.10	2.80
CPWS63	3.80	8.00	3.50

## 9 MEASUREMENT POINT

Bottom electrode		Unit : mm	
TYPE	DIM	A	B
		CPWS10	0.80±0.05
CPWS16	1.35±0.05	0.35±0.05	
CPWS21	1.80±0.05	0.35±0.05	
CPWS32	2.90±0.05	0.35±0.05	
CPWS50	4.50±0.05	1.15±0.05	
CPWS63	5.90±0.05	1.60±0.05	



⊕ Current Terminal  
 ⊖ Voltage Terminal

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## 10 REVISION HISTORY

REVISION	DATE	CHANGE NOTICE	DESCRIPTION
Version.1	10.12.2019		Initial Release
Version.2	19.06.2020		Add new part CPWS10, 16, 21, 32, 50 Revise clause 2 Part Numbering System Revise clause 3.1.1 Resistor Rated Power Revise clause 3.10 TCR table Revise clause 4 Marking on product Add clause 4.1.2, 4.1.3 Revise clause 5.1 Dimension Revise clause 6.1 item Dielectric withstand voltage, item Joint Strength of solder Revise clause 6.4.1 Single pulse load Revise clause 7.2.2 and 7.2.3 and dimension table Revise clause 7.3.2 tape and reel quantity Revise clause 8 Land Pattern dimension Revise clause 9 Measurement point dimension
Version 3	02.06.2023		Revise clause 3.8 Product Assurance



Product Specification

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