

YCN Series (Reverse Concave Type)

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1. SCOPE

- 1.1 This specification is applicable to Lead-free and Halogen-free of RoHS directive for YCN series reverse concave type thick film chip resistors array.
- 1.2 The product is for general electronic purpose.

2. PART NUMBERING SYSTEM

Part Numbering is made in accordance with the following system:

YCN	102	R	<u> </u> -	XXX			J	K
Туре	Size	Terminal Type		No	minal Resistance		Resistance Tolerance	Packaging
I Thick Film Chin II	102 - 0402 x 2 104 - 0402 x 4	R: Reverse		5% (3-Digit)	EX. $10\Omega = 100$ $4.7\Omega = 4R7$ JUMPER = 000			K = 10,000 pcs Lead Free
	104 - 0402 X 4	Concave Type		1% (4-Digit)	EX. 10.2Ω = 10R2 10KΩ = 1002			Y = 20,000 pcs Lead Free N = 50,000 pcs Lead Free

3. RATING

3.1 Rated Power

3.1.1 Resistor Rated Power

Туре	Rated Power at 70°C	Max. Working Voltage	Max. Overload Voltage	JUMPER (0Ω) Rated Current	JUMPER (0Ω) Resistance Value Z (±5%)
YCN102R (0402x2)	1/16 W	25V	50V	1A	50mΩ MAX.
YCN104R (0402x4)	1/16 W	25V	50V	1A	50mΩ MAX.

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3.2 Power Derating Characteristics

Rated Power shall be the load power corresponding to nominal wattage suitable for continuous use at 70°C ambient temperatures. In case the ambient temperature exceeds 70°C, reduce the load power in accordance with Derating curve in Fig. 1.

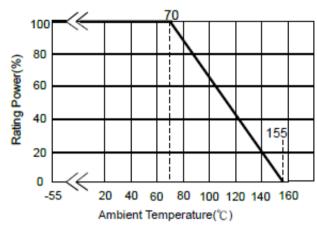


Fig.1 Power Derating Characteristics

3.3 Standard Atmospheric Condition

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient Temperature = $+5^{\circ}$ C to $+35^{\circ}$ C

Relative Humidity = < 85% RH

Air Pressure = 86 kPa to 106kPa

If there may be any doubt about the results, measurement shall be made within the following limits:

Ambient Temperature = $20 \pm 2^{\circ}$ C

Relative Humidity = 60 to 70% RH

Air Pressure = 86 kPa to 106kPa

- 3.4 Operating Temperature Range -55°C to +155°C
- 3.5 Storage Temperature Range -5° C to $+40^{\circ}$ C / < 85% RH
- 3.6 Flammability Rating Tested in accordance to UL-94, V-0
- 3.7 Moisture Sensitivity Level Rating: Level 1
- 3.8 Product Assurance
 ASJ resistors shall warranty 24 months from manufacturing date with control condition.

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- 3.9 ASJ resistors are RoHS-compliant in accordance to RoHS Directive.
- 3.10 Resistance, Resistance Tolerance and Temperature Coefficient of Resistance.

	Rated	Max.	Max.		Resistan	ce Range	Number	Number	JUMPER	JUMPER				
Туре	Type Power vat 70°C \		Overlead	T.C.R. (ppm/℃)	F(±1%) E-24 · E-96	J(±5%) E-24	of Terminals	of Resistors	(0Ω) Rated Current	(0Ω) Resistance Value				
YCN102R	1 W	W 25V 50V	50V	±650	$3\Omega \le R < 10\Omega$	$3\Omega\!\leq\!R\!<\!10\Omega$	4	2	1A	50mΩ				
(0402x2)	16 VV	25V	50V	±250	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	4	2	IA	MAX.				
YCN104R	1 ,,,	1 16 W 25V	05)/	251/	251/	251/	E0\/	±400	$1\Omega \le R < 10\Omega$	$1\Omega \le R < 10\Omega$	8	4	1A	50mΩ
(0402x4)	16 00		50V	±200	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	8	4	1A	MAX.				
Operating Temperature Range					−55°C ~ +155°C									

3.11 Voltage Rating

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

$$E = \sqrt{R \times P}$$

E= Rated voltage (V)

P= power rating (W)

R= Nominal resistance(Ω)

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4. MARKING ON PRODUCT

The nominal resistance shall be marked on the surface of each resistor.

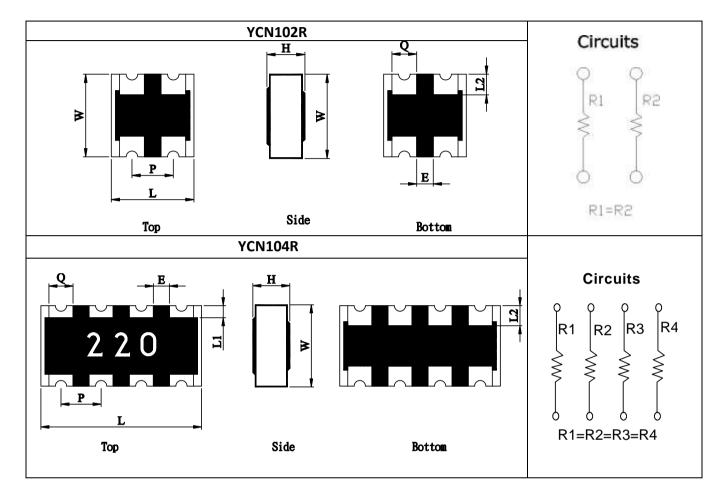
Туре	Resistance Range	Tolerance≦1%	Tolerance > 1%	
YCN102R	ALL			
TCN102R	Jumper=0Ω	No Marking		
VCN104B	ALL	4-digits Marking	3-digits Marking	
YCN104R	Jumper=0Ω		1-digit Marking	

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5. DIMENSION, CONSTRUCTION AND MATERIAL

5.1 Dimension



Unit: mm

TYPE DIM	L	W	Н	L1	L2	Р	Q	E
YCN102R (0402x2)	1.00±0.10	1.00±0.10	0.30±0.10	0.15±0.10	0.25±0.10	(0.50)	0.30±0.10	0.15±0.10
YCN104R (0402x4)	2.00±0.10	1.00±0.10	0.45±0.10	0.15±0.10	0.25±0.10	(0.50)	0.30±0.10	0.15±0.10

5.2 Plating Thickness:

 $Ni:\,\geqq 2~\mu m$

Sn (Tin) : \geq 3 μ m Sn (Tin) : Matte Sn

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6. RELIABILITY TEST

6.1 Electrical Performance Test

ITEN A	Conditions	Specifications				
ITEM	Conditions	Resistors	Jumper			
Temperature Coefficient of Resistance	$TCR(ppm/^{\circ}C) = \frac{(R2-R1)}{R1(T2-T1)}x10^{6}$ R1: Resistance at room temperature R2: Resistance at -55°C or +125°C T1: Room temperature T2: Temperature -55°C or +125°C Refer to JIS-C5201-1 4.8	Refer item 3.10 table.	NA			
	Applied 2.5 times rated voltage for 5 seconds and release the load	1%:△R=±1.0%	50mΩ			
Short Time Overload	for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications)	5% : △R=±2.0%	Lower			
	Refer to JIS-C5201-1 4.13					
Insulation Resistance	Put the resistor in the fixture, add 100 VDC in + ,- terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material. Refer to JIS-C5201-1 4.6 Metal black measuring point A linsulating plate measuring point B Pressurizing by spring R0.5mm Pressurizing by spring R0.5mm	≧10°Ω				
Dielectric Withstand Voltage	Put the resistor in the fixture, add 300 VAC in +,- terminal for 60 sec. Refer to JIS-C5201-1 4.7	No short or burned on the appearance	2.			
Intermittent Overload	Put the tested resistor in chamber under temperature 25±2°C and load 2.5 times rated DC voltage for 1 sec on , 25 sec off , 10000^{+400}_{0} test cycles, then it be left at no-load for 1 hour , then measure its resistance variance rate.	△R=±5.0%	50mΩ Lower			
	Refer to JIS-C5201-1 4.13					

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6.2 Mechanical Performance Test

ITEN 4			Conditions	Specification	Specifications			
ITEM			Conditions	Resistors	Jumper			
Resistance to Solvent	for 5 m		nersed into isopropyl alcohol of 20~25°C sistor is left in the room for 48 hr , then riance rate.	△R=±0.5%	50mΩ Lower			
	Refer to	JIS-C5201-1 4.29						
	The te and at or lon	fter finish each step	test): Id be subject in the following procedure, p, it should be left for a duration of 2 hours tre of 30°C or lower and a humidity of 70%		50mΩ Lower			
	Step	Procedure	Environmental test condition					
	1	Resistance measuring	Room temperature					
	2	Baking	125°C, 24 hours					
	3	Humidification	85°C,85%,168 hours					
	4	4 Reflow (1) Reflow temperature curve and component surface temperature Table 1						
	5	Humidification	85°C,65%,24 hours					
	6	Reflow (2)	Reflow temperature curve and component surface temperature Table 2					
	7	Resistance measuring	Room temperature					
	@Reflo	w temperature cur	ve					
Resistance to Soldering Heat	120 200 Temperature(℃)	180°C	Peak 260 *5 °C 230°C Or Higher Pre-Beeting Zone 90 ± 30 3 30 ± 10 3 Soldering Zone Beeting troe					
		oonent surface tem		<u> </u>				
	T	able 1 Description	example in specification document(1)	1				
		erature-retaining ::230°C or higher	Peak temperature temperature the component body surface during preheating					
		30 seconds	240°C 150 to 160 °C]				

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ITEM				Conditions	Specifications				
ITEIVI			<u>'</u>	Conditions		Resistors	Jumper		
						No evidence of electrode damage.			
	Tab	ole 2 Descrip	tion exa	ample in specifica	tion document(2)	No side conductive peel off.			
	Temp	Temperature Ter		Temperature Temperature-retainin time		_	Temperature measured at the component body surface during preheating		
	220°C	or higher	9	90 seconds 150 to 10					
		or higher	6	0 seconds					
	240°C	or higher	ļ	5 seconds					
	F	Peak		245°C					
	The tester after finish lower at a or lower.	sh each step a temperatu	hould be o, it shou ure of 30	e subject in the fo Ild be left for a du O°C or lower and a	llowing procedure, and iration of 2 hours or i humidity of 70% RH				
	Step	Proced	ure	Environmen	tal test condition				
	1 1	Resista: measur		Room t	emperature				
	2	Bakin	ng	125°C	, 24 hours				
	3	Humidific	cation	85°C,85	%, 168 hours				
	4	Solder po	t test	260±3°	°C , 10 sec				
	5	Place	d	85°C,65	5%,24 hours				
	6	Solder po	t test	260±3°	°C , 10 sec				
	7	Resistar measur		Room t	emperature				
	Refer to J	SS-00254-5) IIS-C5201-1							
	105°C, huduration temperat	ested resistoumidity of 10 of 4 hours ture for 2 ho	00% RH, Then aft	apparatus of PCT and pressure of the tested nore.	1.Test item 1: Solder coverage over 95%				
Solderability	Test method: © solder pot test: The resistor be immersed into solder pot in temperature 235±5°C fo 2 sec, then the resistor is left as placed under microscope to observed its solder area. Refer to JIS-C5201-1 4.17								

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ITEN 4	Conditions	Specifications	Specifications			
ITEM	Conditions	Resistors	Jumper			
Joint Strength of Solder	Preconditioning: Put the tested resistor in the apparatus of PCT, at a tem 105°C, humidity of 100% RH, and pressure of 1.22×10 ⁵ P duration of 4 hours. Then after left the tested resistor in temperature for 2 hours or more. Test method: ©Test item 1 (Adhesion): A static load using a R0.5 scratch tool shall be applied or the component and in the direction of the arrow and he seconds and under load measure its resistance variance Load = 20N Cross—sectional variations is precised by the second of the	Resistors Test item 1: 1.△R%=±1.0% Test item 2: 1.△R%=±1.0% Test item 3: (1).Adhesion After application of temperature cycle, adhesion should be 50% or more of initial strength.	Jumpe 50mΩ Lower			
	Chip resistor S230 Pressurize	rate .				
	OHM Meter					
	Refer to JIS-C5201-1 4.33					
	©Test item 3 (Endurance measurement): Put the tested resistor in the chamber under the temporal cycle which shown in table 1 shall be repeated 1000± consecutively. Then separate follow test item 1 and test item 2 50% condition to temporal discrete resistance variance rate. Table 1 Temperature cycle test condition Testing conductive temporature Lowest temperature 105±5°6 Highest temperature	4 times st, dition				

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ITEM	Conditions	Specifications				
IIEIVI	Conditions	Resistors	Jumper			
		1.Solder coverage over 95%.2.The underlying material (such as cera not be visible at the crest corner are electrode.	•			

6.3 Environmental Test

ITEN 4	Canaditiana		Specifications		
ITEM	Conditions		Resistors	Jumper	
Resistance to Dry Heat	Put tested resistors in chamber under ten 1000±4 hours. Then leaving in room temp and measure its resistance variance rate . Refer to JIS-C5201-1 4.25	•	1%:△R=±1.0% 5%: △R=±2.0%	50mΩ Lower	
	Put the tested resistor in the thermal short temperature cycle which shown in the fol repeated 300 times consecutively. Then let the room temperature for 1 hours, and my variance rate.	△R=±1.0%	50mΩ Lower		
Thermal Shock		Testing Condition			
	Lowest Temperature	-55±5°C			
	Highest Temperature	125±5°C			
	Temperature-retaining time	15 minutes each			
	Refer to MIL-STD 202 Method 107 Put the tested resistor in the chamber under relative humidity 90~95% and load the ra	1%:△R=±2.0% 5%: △R=±3.0%	50mΩ Lower		
Loading Life in Moisture	on, 30 minutes off, total 1000 hours. Ther in room temperature for 60 minutes, and variance rate.	570 . ZIN-13.070	Lower		
	Refer to JIS-C5201-1 4.24				
Load Life	Put the tested resistor in chamber under to load the rated voltage for 90 minutes on, hours. Then leaving the tested resistor in minutes, and measure its resistance varia Refer to JIS-C5201-1 4.25	1%:△R=±2.0% 5%: △R=±3.0%	50mΩ Lower		

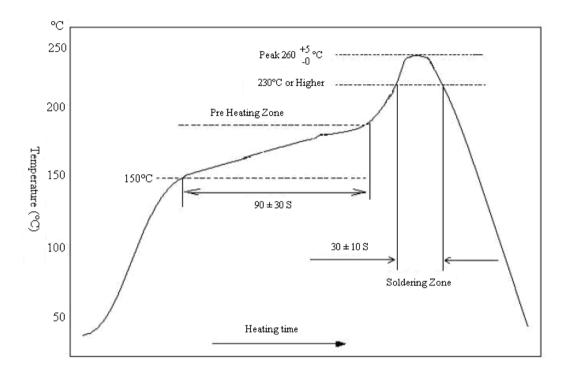
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6.4 Soldering Profile

(Technical application notes: This is for recommendation, customer please perform adjustment according to actual application.)

6.4.1 Lead Free IR Reflow Soldering Profile



6.4.2 Soldering Iron: temperature 350°C±10°C, dwell time shall be less than 3 sec.

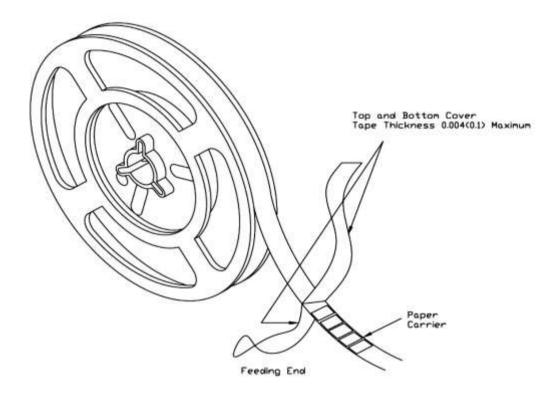
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7. TAPING

7.1 Structure of Taping

Paper Carrier

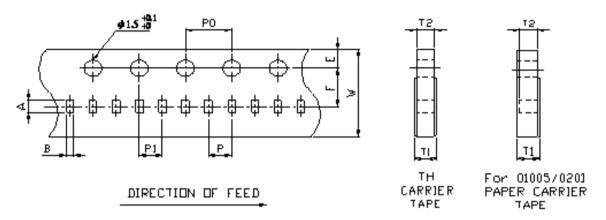


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7.2 Dimension

7.2.1 Dimension of Punched Paper Tape Carrier System



Remark : Pitch tolerance over any 10 pitches of Po is \pm 0.2 mm

	Array/Network		Dimensions (mm)										
	Туре	Α	В	W	E	F	T1	T2	Р	P0	10xP0	P1	
Packaging	YCN102R	1.20±0.10	1.20±0.10	8.00±0.20	1.75±0.10	3.50±0.05	$0.40^{+0.2}_{-0}$	0.40±0.10	2.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05	
	YCN104R	2.20±0.10	1.20±0.10	8.00±0.20	1.75±0.10	3.50±0.05	$0.60^{+0.2}_{-0}$	0.60±0.10	2.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05	

7.3 Packaging

7.3.1 Taping

Quantity - Tape and Reels

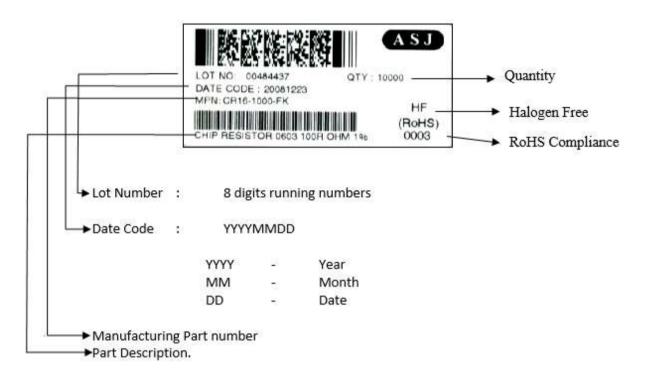
Packaging Qty(pcs/reel)						
Size/Type(Array)	Size/Type(Array) 2mm pitch 4mm pitch Reel					
V01400D	10,000		7" Reel			
YCN102R YCN104R	20,000		10" Reel			
YCN104K	50,000		13" Reel			

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7.3.2 Identification

Production label that indicates the 8 digits lot number, product type, resistance value and tolerance shall be pasted on the surface of each reel.



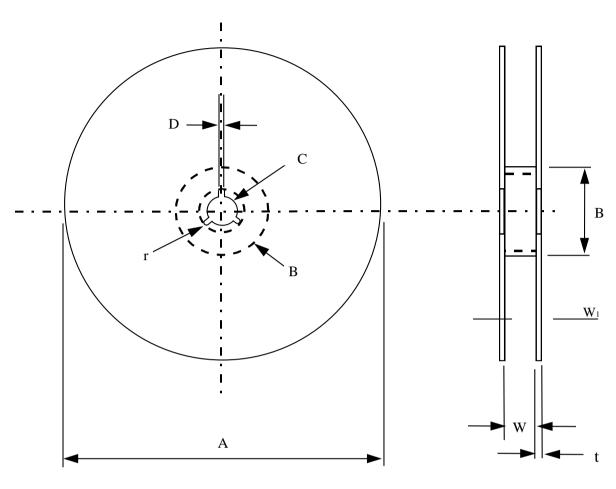
7.3.3 Packaging Reel Box

Dimension	Reel Box	Number of Reels
185 × 60 × 186 mm	25K Box	5
185 × 120 × 186 mm	50K Box	10

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7.3.4 Reel Dimensions



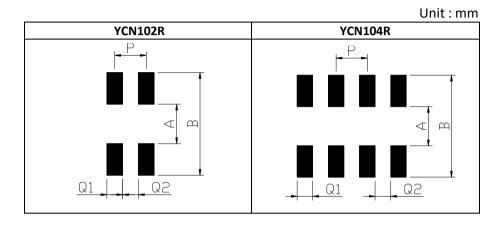
Model	Α	В	С	D	W	W_1	t	r
7"Reel (5K) (except 0402 10K)	φ178±2.0	φ60min	13± 0.2	φ2.0± 0.5	11± 0.1	14.4 max	1.0± 0.1	1.0
7"Reel (4K)	φ178±2.0	φ60min	13± 0.2	φ2.0± 0.5	13±1.0	14.4 max	1.2± 0.1	1.0
10"Reel (10K)	φ254±2.0	φ60min	13± 0.2	φ2.0± 0.5	11± 1.0	14.4 max	1.5± 0.1	1.0
13"Reel (20K, 50K)	ф330±2.0	φ60min	13± 0.2	ф2.0± 0.5	11± 1.0	14.4 max	2.1± 0.1	-
13"Reel (20K)	ф330±1.0	φ100±1	13.5±0.5	2~3±0.5	10±0.5	1	-	-

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8. SURFACE MOUNT LAND PATTERNS DESIGN (FOR REFLOW SOLDERING)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



TYPE	А	В	Р	Q1	Q2
YCN102R	0.50	2.00	0.50	0.28	0.22
YCN104R	0.50	2.00	0.50	0.28	0.22

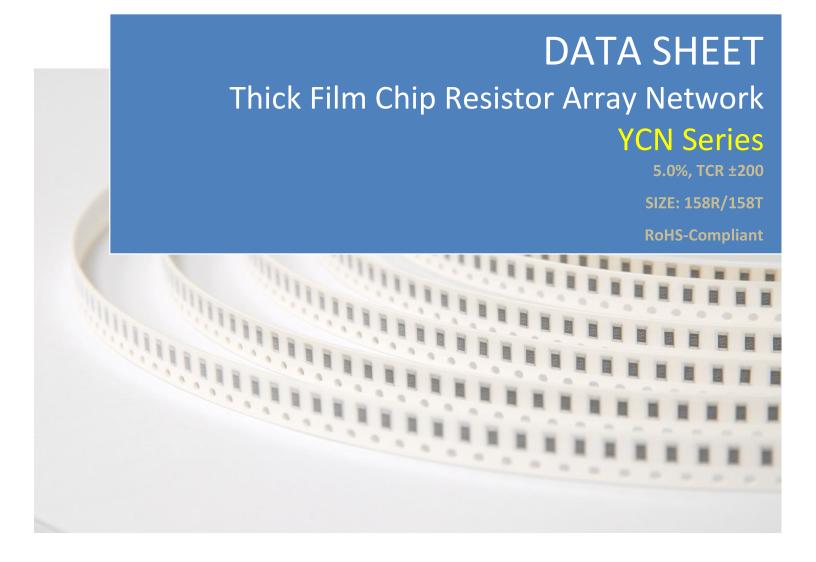
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9. REVISION HISTORY

Revision	Date	Change Notification	Description
Version 1	24.07.2023		Initial Release





THICK FILM CHIP RESISTOR ARRAY NETWORK

YCN Series

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1. SCOPE

1.1 This specification is applicable to Lead-free and Halogen-free of RoHS Directive for YCN series thick film chip resistor array network.

1.2 The product is for general electronic purpose.

2. PART NUMBERING SYSTEM

Part Numbering is made in accordance with the following system:

YCN	158R		100	J	L
Туре	Size (Inch/mm)	Nor	ninal Resistance	Resistance Tolerance	Packaging
Thick Film Chip Resistors Array	158R (0612/1632) 158T (0612/1632)	3-Digit	EX. 10Ω = 100 4.7Ω = 4R7 JUMPER = 000	J = ±5% Z = Zero ohm *For 3-digit only Ex. YCN158R-000-ZL	L = 5,000 pcs Lead Free K = 10,000 pcs Lead Free Y = 20,000 pcs Lead Free

3. RATING

3.1 Rated Power

3.1.1 Resistor Rated Power

Туре	Rated Power at 70°C	Max. Working Voltage	Max. Overload Voltage	JUMPER Rated Power	JUMPER Resistance Value
YCN158R	1/16 W	25V	50V	1A	50mΩ MAX.
YCN158T	1/16 W	25V	50V	1A	50mΩ MAX.

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3.2 Power Derating Characteristics

Rated Power shall be the load power corresponding to nominal wattage suitable for continuous use at 70° C ambient temperatures. In case the ambient temperature exceeds 70° C, reduce the load power in accordance with Derating curve in Fig. 1.

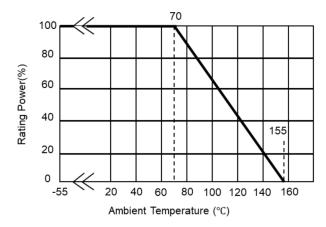


Fig.1 Power Derating Characteristics

3.3 Standard Atmospheric Condition

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient Temperature = $+5^{\circ}$ C to $+35^{\circ}$ C

Relative Humidity = < 85% RH

Air Pressure = 86 kPa to 106kPa

If there may be any doubt about the results, measurement shall be made within the following limits:

Ambient Temperature = $20 \pm 2^{\circ}$ C

Relative Humidity = 60 to 70% RH

Air Pressure = 86 kPa to 106kPa

- 3.4 Operating Temperature Range -55°C to +155°C
- 3.5 Storage Temperature Range -5° C to $+40^{\circ}$ C / <85% RH
- 3.6 Flammability Rating Tested in accordance to UL-94, V-0
- 3.7 Moisture Sensitivity Level Rating: Level 1
- 3.8 Product Assurance

ASJ resistors shall warranty 24 months from manufacturing date with control conditions.

3.9 ASJ resistors are RoHS-compliant in accordance to RoHS Directive.



Product Specification

THICK FILM CHIP RESISTOR ARRAY NETWORK

YCN Series

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3.10 Resistance, Resistance Tolerance and Temperature Coefficient of Resistance.

Туре	Rated Power at 70℃	Max. Working Voltage	Max. Overload Voltage	T.C.R. (ppm/℃)	Resistance J(±5%) E-12	Number of Terminals	Number of Resistors	JUMPER Rated Power	JUMPER Resistance Value
YCN158R	1/16 W	25V	50V	±200	47Ω~1ΜΩ	10	8	1A	50mΩ MAX.
YCN158T	1/16 W	25V	50V	±200	33Ω~1ΜΩ	10	8	1A	50mΩ MAX.
Operating Temperature Range					-55°C ~+1	.55°C			

3.11 Rated Voltage

The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following.

$$E = \sqrt{R \times P}$$
E= Rated voltage (V)
P= power rating (W)
R= Nominal resistance(Ω)

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4. MARKING ON PRODUCT

The nominal resistance shall be marked on the surface of each resistor.

Part Number	Color	Marking on Product
YCN158R	White	E-24 Series: 3 digits First two digits for significant figure and 3 rd digit
YCN158T	White	for number of zeros.

4.1 Numeric Numbering

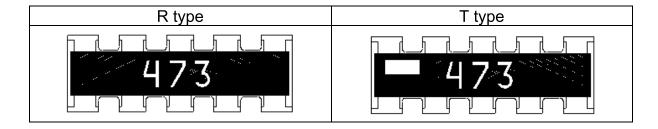
4.1.1 5% Tolerance: *Three Numerals Marking*

First 2 digits are significant figures, third digit is number of zeros. Letter R is decimal point.

Example

Nominal Resistance	Marking	Remarks
1Ω	1R0	1 X 10 ⁰ = 1
10 Ω	100	10 X 10 ⁰ = 1 0
100 Ω	101	10 X 10 ¹ = 1 00
4.7K Ω	472	$47 \times 10^2 = 47 \times 00$
47Κ Ω	473	47 X 10 ³ = 47 000
470K $Ω$	474	47 X 10 ⁴ = 47 0000
4.7 M Ω	475	47 X 10 ⁵ = 47 00000

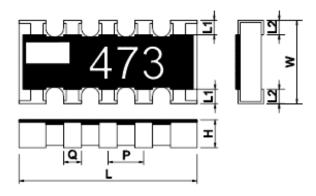
4.1.2 Chip Resistors Network Appearance:



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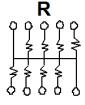
5. DIMENSION

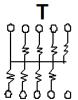
5.1 Dimension



		Dimensions						
Type			Inc	hes (Millimeter	rs)			
	L	W	Н	L ₁	L ₂	Р	Q	
YCN158R	3.20±0.20	1.60±0.15	0.55 ± 0.10	0.30±0.15	0.30±0.15	0.64±0.10	0.32±0.10	
YCN158T	5.20±0.20	1.60±0.15	0.55 ± 0.10	0.30±0.13	0.50±0.15	0.64±0.10	0.32±0.10	

5.2 Circuit diagram





5.3 Plating Thickness

 $Ni: \geqq 2\mu\mathrm{m}$

Sn (Lead Free): $\ge 3 \mu m$ Sn (Tin): Matte Sn

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6. RELIABILITY TEST

6.1 Electrical Performance Test

ltem	Conditions	Specifications
iteiii	Conditions	Resistors
Temperature Coefficient of Resistance	Refer to JIS-C5201-1 4.8 $TCR(ppm/^{\circ}C) = \frac{(R2-R1)}{R1(T2-T1)}x10^{6}$ R1: Resistance at room temperature R2: Resistance at -55°C or +125°C T1: Room temperature T2: Temperature -55°C or +125°C	Refer item 3.10
Short Time Overload	Refer to JIS-C5201-1 4.13 Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications)	△R=±2.0%
Insulation Resistance	Put the resistor in the fixture, add 100 VDC in + ,- terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material. Metal black measuring Metal plate measuring point Base material Metal plate measuring point Metal plate m	
Dielectric Withstand Voltage	Refer to JIS-C5201-1 4.7 Put the resistor in the fixture, add 300 VAC in +,- terminal for 60 sec.	No short or burned on the appearance.
Intermittent Overload	Refer to JIS-C5201-1 4.13 Put the tested resistor in chamber under temperature $25\pm2^{\circ}$ C and load 2.5 times rated DC voltage for 1 sec on , 25 sec off , 10000^{+400}_{-0} test cycles, then it be left at no-load for 1 hour , then measure its resistance variance rate.	△R=±5.0%

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6.2 Mechanical Performance Test

lh	Conditions	Specifications
Item	Conditions	Resistors
Resistance to	Refer to JIS-C5201-1 4.29	△R=±0.5%
Solvent	The tested resistor be immersed into isopropyl alcohol of 20^{25} °C for 5 minutes, then the resistor is left in the room for 48 hrs, then measure its resistance variance rate.	
Resistance to Soldering Heat	Refer to JIS-C5201-1 4.18 © Test method 1 (Solder pot test): The tested resistor be immersed into molten solder of 260^{+5}_{-0} °C for 10 seconds. Then the resistor is left in the room for 1 hour.	Test item 1: (1).Variance rate on resistance
	\odot Test method 2 (Solder pot test): The tested resistor be immersed into molten solder of 260^{+5}_{-0} °C for 30 seconds. Then the resistor is left as placed under microscope to observe its solder area.	(2).The underlying material (such as ceramic) shall not be visible at the crest corner area of the electrode.
Solderability	Refer to JIS-C5201-1 4.17 Preconditioning: Put the tested resistor in the apparatus of PCT, at a temperature of 105°C, humidity of 100% RH, and pressure of 1.22×10 ⁵ Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more. Test method: The tested resistor be immersed into solder pot in temperature 230±5°C for 2 sec, then the resistor is left as placed under microscope to observed its solder area.	Solder coverage over 95%
Joint Strength of Solder	© Bending Strength: Solder tested resistors on to PC board. add force in the middle down, and under load measure its resistance variance rate. D = 5 mm Resistor Testing circuit board Supporting jig Chip resistor Pressurize OHM Meter	△R%=±1.0%

THICK FILM CHIP RESISTOR ARRAY NETWORK

YCN Series

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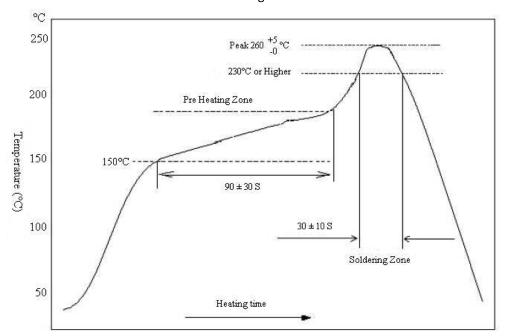
6.3 Environmental Test

Item	Condition	Specifications	
			Resistors
Resistance to Dry Heat	Refer to JIS-C5201-1 4.25 Put tested resistors in chamber under 1,000±4 hours. Then leaving in room t and measure its resistance variance ra	△R=±2.0% s,	
	Refer to MIL-STD 202 Method 107		△R=±1.0%
Thermal Shock	Put the tested resistor in the thermal at temperature cycle which shown in the repeated 300 times consecutively. The in the room temperature for 1 hour, a variance rate.	or	
	Testing Cond		
	Lowest Temperature	-55±5°C	
	Highest Temperature	125±5°C	
	Temperature-retaining time	15 minutes each	
Loading Life in Moisture	Refer to JIS-C5201-1 4.24 Put the tested resistor in the chamber relative humidity 90~95% and load the minutes on, 30 minutes off, total 1000 tested resistor in room temperature feits resistance variance rate.		
Load Life	Refer to JIS-C5201-1 4.25 Put the tested resistor in chamber und load the rated voltage for 90 minutes 1000 hours. Then leaving the tested refor 60 minutes, and measure its resist		

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6.4 Soldering Profile (Technical application notes : This is for recommendation, customer please perform adjustment according to the actual application)

6.4.1 Lead-Free IR Reflow Soldering Profile



Remark: The peak temperature of soldering heat is $260^{+5}_{-0}\,$ °C for 10 seconds.

6.4.2 Soldering Iron : temperature 350°C±10°C , dwell time shall be less than 3 sec

THICK FILM CHIP RESISTOR ARRAY NETWORK

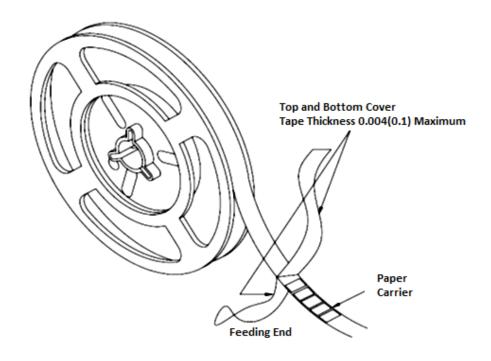
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7. TAPING

7.1 Structure of Taping

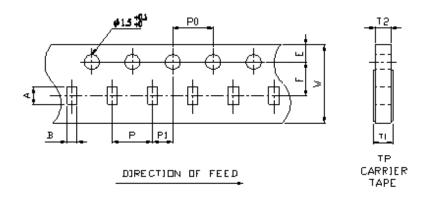
Paper Carrier



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7.2 Dimension

7.2.1 Dimension of Punched Paper Tape Carrier System



Remark : Pitch tolerance over any 10 pitches of Po is \pm 0.2 mm

	Array/Network					Din	nensions (m	nm)				
Packaging	Туре	Α	В	W	E	F	T1	T2	Р	PO	10xP0	P1
	YCN158R YCN158T	3.50±0.20	1.90±0.20	8.00±0.20	1.75±0.10	3.50±0.05	0.75 ^{+0.2}	0.75±0.10	4.00±0.10	4.00±0.05	40.00±0.20	2.00±0.05

7.3 Packaging

7.3.1 Taping

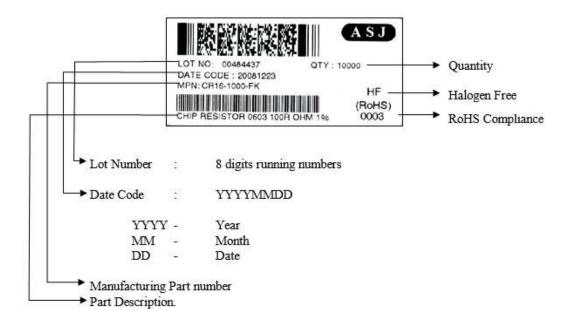
Quantity – Tape and Reels

Code	Quantity	Reel
VCN1EQD	5000	7"
YCN158R YCN158T	10000	10"
ICN1281	20000	13"

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7.3.2 Identification

Production label that indicates the 8 digits lot number, product type, resistance value and tolerance shall be pasted on the surface of each reel.

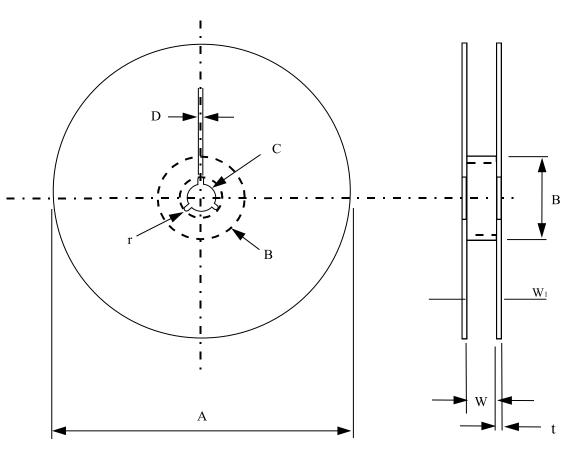


7.3.3 Packaging Reel Box

Dimension	Reel Box	Number of Reels
185 × 60 × 186 mm	25K Box	5
185 × 120 × 186 mm	50K Box	10

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Model	Α	В	С	D	W	W_1	t	r
7"Reel (5K) (except 0402 10K)	ф178±2.0	ф80min	13± 0.2	φ2.0± 0.5	11± 0.1	14.4 max	1.0± 0.1	1.0
7"Reel (4K)	φ178±2.0	φ60min	13± 0.2	φ2.0± 0.5	13±1.0	14.4 max	1.2± 0.1	1.0
10"Reel (10K)	φ254±2.0	φ60min	13± 0.2	φ2.0± 0.5	11± 1.0	14.4 max	1.5± 0.1	1.0
13"Reel (20K, 50K)	ф330±2.0	φ60min	13± 0.2	ф2.0± 0.5	11± 1.0	14.4 max	2.1± 0.1	-
13"Reel (20K)	φ330±1.0	φ100±1	13.5±0.5	2~3±0.5	10±0.5	-	-	-

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8. SURFACE MOUNT LAND PATTERNS DESIGN (FOR REFLOW SOLDERING)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

Unit: mm

	P	\bowtie		DIM TYPE	А	В	С	D	Р
			B 4	YCN158R					
C		D _		YCN158T	2.6	1.0	0.34	0.30	0.64

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9. REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version.1	13.02.2015		Initial Release
Version.2	14.12.2018		Datasheet update
Version.3	15.08.2019		Update resistance range in clause 3.10
Version.4	15.09.2020		Revise clause 3.5
Version.5	15.09.2021		Revise clause 2 Part Numbering System
Version 6	15.09.2023		Revise clause 3.8 Product Assurance